



This is an abbreviated version of this data-sheet. For the complete version, please contact our Cypress Marketing Representative.

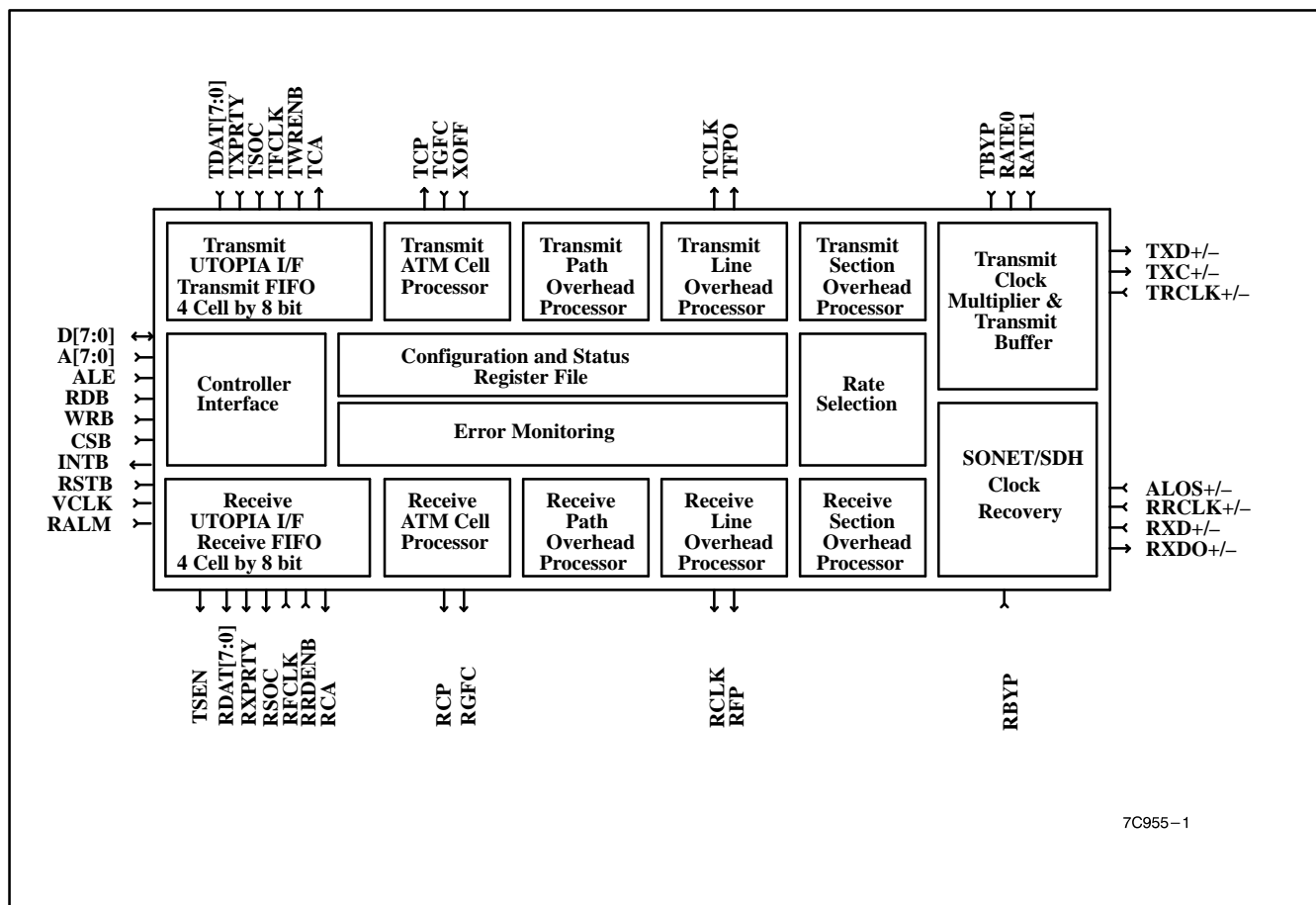
ADVANCED INFORMATION

CY7C955

ATM – SONET/SDH Transceiver

Features

- WAN and LAN ATM physical layer device
- Provides complete physical layer transport of ATM cells at
 - STS-3c/ STM-1 rate of 155.52MHz
 - STS-1 rate of 51.84MHz
- Compliant with ATM Forum User Network Interface 3.1 specification
- UTOPIA ATM interface
- ATM cell processing including
 - HEC generation/verification
 - Cell scrambling/descrambling
 - Rate adaption/idle cell filtering
 - Local Flow Control
 - Cell alignment
- SONET frame processing including
 - Compliant with, Bellcore GR-253, TL105 L432, and G.709
 - Frame generation/recovery
 - SONET scrambling/descrambling
 - Frequency justification/pointer processing
- Complete line interface
 - Clock and data recovery
 - Transmit timing derived from receiver or byte-rate source
 - No external PLL components
 - SONET compliant PLL
 - 100K ECL compatible I/O
- Alarm indications including
 - Loss Of Signal
 - Out Of Frame, Loss Of Frame
 - Line Far End Receive Failure
 - Line Alarm Indication Signal
 - B1 Parity Error
 - Loss Of Cell Alignment
 - Loss Of Receive Data
- Controller interface for internal interrupt and configuration registers
 - Error monitoring
 - Status indication
 - Device configuration
- 0.65μ low-power CMOS
- 128 pin PQFP



7C955-1

Document #: 38-00417