Static RAMs

Size	Organization	Pins (DIP)	Part Number	SMD Number	Speed (ns)	$I_{CC}/I_{SB}/I_{CCDR}$ (mA @ ns)	883 Availability
64	16 x 4—Non-Inverting	16	CY7C190	5962-89694	t_{AA} = 25	70 @ 25	Now
64	16 x 4—Inverting	16	CY27S037/A		t_{AA} = 25, 35	100 @ 35	Now
1K	256 x 4	22	CY7C122	5962-88594	t_{AA} = 25, 35	90 @ 25	Now
1K	256 x 4	22	CY93422A/93L422A	5962-88594	t_{AA} = 45, 55, 60, 75	90 @ 55	Now
4K	1K x 4—CS Power-Down	18	CY7C148	5962-87513	t_{AA} = 35, 45	110/10 @ 35	Now
4K	1K x 4—CS Power-Down	18	CY2148	5962-87513	t_{AA} = 45, 55	140/25 @ 45	Now
4K	1K x 4	18	CY7C149		t_{AA} = 35, 45	110 @ 35	Now
4K	1K x 4	18	CY2149		t_{AA} = 45, 55	140 @ 45	Now
4K	1K x 4—Separate I/O	24S	CY7C150	5962-88588	t_{AA} = 12, 15, 25, 35	100 @ 15	Now
8K	1K x 8—Dual Port	48	CY7C130/31	5962-86875	t_{AA} = 35, 45, 55	120/40 @ 45	Now
8K	1K x 8—Dual-Port Slave	48	CY7C140/41	5962-86875	t_{AA} = 35, 45, 55	120/40 @ 45	Now
16K	2K x 8—CS Power-Down	24S	CY7C128A	5962-89690	t_{AA} = 20, 25	125 @ 20	Now
16K	2K x 8—CS Power-Down	24	CY6116A/7A	5962-89690	t_{AA} = 20, 25	125 @ 20	Now
16K	2K x 8—CS Power-Down	24S	CY7C128A	84036	t_{AA} = 35, 45, 55	125/40 @ 25	Now
16K	16K x 1—CS Power-Down	20	CY7C167A	84132	t_{AA} = 20	70/20 @ 25	Now
16K	4K x 4—CS Power-Down	20	CY7C168A	5962-86705	t_{AA} = 20, 25, 35, 45	100/20 @ 25	Now
16K	4K x 4	20	CY7C169A		t_{AA} = 20, 25, 35, 40	100/20 @ 35	Now
16K	4K x 4—Separate I/O, Power-Down	24S	CY7C172A	5962-89790	t_{AA} = 20, 25, 35, 45	90 @ 20	Now
16K	2K x 8—Dual-Port	48	CY7C132/36	5962-90620	t_{AA} = 35, 45, 55	170/65 @ 35	Now
16K	2K x 8—Dual-Port Slave	48	CY7C142/46	5962-90620	t_{AA} = 35, 45, 55	120/40 @ 45	Now
32K	4K x 8—Dual-Port	48	CY7B134	5962-93001	t_{AA} = 25, 35	280 @ 25	Now
32K	4K x 8—Dual-Port	52	CY7B135	5962-93001	t_{AA} = 25, 35	280 @ 25	Now
32K	4K x 8—Dual-Port Semaphores	52	CY7B1342		t_{AA} = 25, 35	280 @ 25	Now
32K	4K x 8—Dual-Port Semaphores Int, Busy	68	CY7B138		t_{AA} = 25, 35	280 @ 25	Now
32K	4K x 9—Dual-Port Semaphores Int, Busy	68	CY7B139		t_{AA} = 25, 35	280 @ 25	Now
64K	8K x 8—CS Power-Down	28S	CY7C185A	5962-38294	t_{AA} = 20, 25, 35, 45	125 @ 20	Now
64K	8K x 8—CS Power-Down	28S	CY7C185A	5962-89691	t_{AA} = 20, 25	125 @ 20	Now
64K	8K x 8—CS Power-Down	28S	CY7C185A	5962-85525	t_{AA} = 35, 45	100/20/1 @ 45	Now
64K	8K x 8—CS Power-Down	28S	CY7B185	5962-91594	t_{AA} = 10, 12, 15	145/50 @ 15	Now
64K	8K x 8—CS Power-Down	28	CY7C186A	5962-38294	t_{AA} = 20, 25, 35, 45	125 @ 20	Now
64K	8K x 8—CS Power-Down	28	CY7C186A	5962-89691	t_{AA} = 20, 25	125 @ 20	Now
64K	8K x 8—CS Power-Down	28	CY7C186A	5962-85525	t_{AA} = 35, 45, 55	100/20/1 @ 45	Now
64K	16K x 4—CS Power-Down	22S	CY7C164A	5962-89692	t_{AA} = 20, 25	90 @ 20	Now
64K	16K x 4—CS Power-Down	22S	CY7C164A	5962-86859	t_{AA} = 35	70/20/1 @ 35	Now
64K	16K x 4—CS Power-Down	24S	CY7C166A	5962-89892	t_{AA} = 20, 25	90 @ 20	Now
64K	16K x 4—Output Enable	24S	CY7C166A	5962-86859	t_{AA} = 35	70/20/1 @ 35	Now
64K	16K x 4—Separate I/O, T-write	28S	CY7C161A	5962-90594	t_{AA} = 20, 25, 35	70/20/1 @ 35	Now
64K	16K x 4—Separate I/O	28S	CY7B162	5962-92172	t_{AA} = 12, 15	135/50 @ 15	Now
64K	64K x 1—CS Power-Down	22S	CY7C187A	5962-86015	t_{AA} = 20, 25, 35	70/20/1 @ 35	Now
64K	8K x 8—Dual-Port Semaphores Int, Busy	68	CY7B144		t_{AA} = 25, 35	280 @ 25	Now
64K	8K x 9—Dual-Port Semaphores Int, Busy	68	CY7B145		t_{AA} = 25,35	280 @ 25	Now
256K	32K x 8—CS Power-Down	28	CY7C198	5962-88662	t_{AA} = 15, 20, 25, 35, 45	180/40 @ 20	Now
256K	32K x 8—CS Power-Down	28S	CY7C199	5962-88662	t_{AA} = 15, 20, 25, 35, 45	180/40 @ 20	Now
256K	64K x 4—CS Power-Down	24S	CY7C194	5962-88681	t_{AA} = 15, 20, 25, 35, 45	150/40 @ 20	Now
256K	64K x 4—CS, OE	28S	CY7C195	5962-89524	t_{AA} = 15, 20, 25, 35, 45	120/25 @ 25	Now
256K	64K x 4—CS PD + OE/CE2	28S	CY7C196	5962-93225	t_{AA} = 15, 20, 25, 35, 45	150/40 @ 20	Now
256K	64K x 4—Separate I/O	28S	CY7C192	5962-89935	t_{AA} = 25, 35	150/40 @ 20	Now
256K	256K x 1—CS Power-Down	24S	CY7C197	5962-88725	t_{AA} = 25, 35	150/40 @ 20	Now
1M	256K x 4—CS Power-Down/OE	28S	CY7C1006	5962-91612	t_{AA} = 15, 20, 25	165/40 @ 15	4Q96
1M	256K x 4—Separate I/O, T-Write	32S	CY7C1001		t_{AA} = 15, 20, 25	165/40 @ 15	4Q96
1M	256K x 4—Separate I/O	32S	CY7C1002		t_{AA} = 15, 20, 25	165/40 @ 15	4Q96
1M	1M x 1—CS Power-Down	28S	CY7C1007	5962-92316	t_{AA} = 15, 20, 25	145/40 @ 15	4Q96

PROMs

Size	Organization	Pins	Part Number	SMD Number[1]*	Speed (ns)	I_{CC}/I_{SB} (mA @ ns)	883 Availability
4K	512x8—Registered	24S	CY7C225A	5962-88518(O)	$t_{SA/CO}$ = 25/12, 30/15, 35/20	120	Now
8K	1Kx8—Registered	24S	CY7C235A	5962-88636(O)	$t_{SA/CO}$ = 25/12, 30/15, 40/20	120	Now
8K	1Kx8	24S	CY7C281A	5962-87651(O)	t_{AA} = 30, 45	120	Now
8K	1Kx8	24	CY7C282A	5962-87651(O)	t_{AA} = 30, 45	120	Now
16K	2K x 8—Registered	24S	CY7C245	5962-87529(W)	$t_{SA/CO}$ = 35/15, 45/25	120 @ 35/15	Now
16K	2K x 8—Registered	24S	CY7C245A	5962-89815(W)	$t_{SA/CO}$ = 18/12, 25/12, 35/15	120 @ 18/12	Now
16K	2K x 8—Registered	24S	CY7C245A	5962-88735(O)	$t_{SA/CO}$ = 18/12, 25/12, 35/15	120 @ 18/12	Now
16K	2K x 8	24S	CY7C291	5962-87650(W)	t_{AA} = 25, 35, 50	120 @ 35	Now
16K	2K x 8	24S	CY7C291A	5962-88734(O)	t_{AA} = 25, 30, 35, 50	120 @ 25	Now
16K	2K x 8—CS Power-Down	24S	CY7C293A	5962-88680(W)	t_{AA} = 25, 30, 35, 50	120/30 @ 25	Now
16K	2K x 8—CS Power-Down	24S	CY7C293A	5962-92341(O)	t_{AA} = 25, 30, 35, 50	120/30 @ 35	Now
16K	2K x 8	24	CY7C292		t_{AA} = 35, 50	120 @ 35	Now
16K	2K x 8	24	CY7C292A	5962-88734(O)	t_{AA} = 25, 30, 35, 45, 50	120 @ 30	Now
64K	8K x 8—CS Power-Down	24S	CY7C261	5962-87515(W)	t_{AA} = 25, 35, 45, 55	140/50 @ 25	Now
64K	8K x 8—CS Power-Down	24S	CY7C261	5962-90803(O)	t_{AA} = 25, 35, 45, 55	120/30 @ 35	Now
64K	8K x 8	24S	CY7C263/4	5962-87515(W)	t_{AA} = 25, 35, 45, 55	140 @ 25	Now
64K	8K x 8	24	CY7C263/4	5962-90803(O)	t_{AA} = 25, 35, 45, 55	120 @ 35	Now
64K	8K x 8—Registered	28S	CY7C265	5962-89967(O)	$t_{SA/CO}$ = 15/12, 25/20, 50/25	140 @ 18/15	Now
64K	8K x 8—Registered	28S	CY7C265	5962-89484(W)	$t_{SA/CO}$ = 15/12, 25/20, 50/25	120 @ 50/25	Now
64K	8K x 8—Registered/Diagnostic	28S	CY7C269	5962-90831(O)	$t_{SA/CO}$ = 15/12, 25/20, 50/25	140 @ 15/12	Now
64K	8K x 8—Registered/Diagnostic	28S	CY7C269	5962-90930(W)	$t_{SA/CO}$ = 15/12, 25/20, 50/25	140 @ 15/12	Now
128K	16K x 8—CS Power-Down	28S	CY7C251	5962-89537(W)	t_{AA} = 45, 55, 65	120/35 @ 45	Now
128K	16K x 8	28	CY7C254	5962-89538(W)	t_{AA} = 45, 55, 65	120 @ 45	Now
128K	16K x 8—EPROM Pinout	28	CY27C128		t_{AA} = 45, 55, 70, 90, 120, 150, 200	55/20	Now
256K	16Kx16	44	CY7C276		t_{AA} = 25, 30, 35	250@30	Now
256K	32K x 8—CS Power-Down	28S	CY7C271	5962-89817(W)	t_{AA} = 35, 45, 55	130/40 @ 35	Now
256K	32K x 8—CS Power-Down	28S	CY7C271	5962-93166(O)	t_{AA} = 35, 45, 55	130/40@55	Now
256K	32K x 8—EPROM Pinout	28	CY7C274	5962-89817(W)	t_{AA} = 35, 45, 55	130/40 @ 35	Now
256K	32K x 8—EPROM Pinout	28	CY7C274	5962-93166(O)	t_{AA} = 35, 45, 55	130/40 @ 35	Now
256K	32K x 8—EPROM PInout	28	CY27C256		t_{AA} = 45, 55, 70, 90, 120, 150, 200	55/20	2Q96
256K	32K x 8—Registered	28S	CY7C277	5962-91744(W)	$t_{SA/CO}$ = 40/20, 50/25	130 @ 40	Now
256K	32K x 8—Registered	28S	CY7C277	5962-92155(O)	$t_{SA/CO}$ = 40/20, 50/25	130 @ 40	Now
512K	64K x 8—EPROM Pinout	28	CY7C286	5962-91637(O)	t_{AA} = 60, 70	150 @ 60	Now
512K	64K x 8—EPROM Pin	28	CY7C286	5962-92071(W)	t_{AA} = 60, 70	150 @ 60	Now
512K	64K x 8—Registered	28S	CY7C287	5962-90913(W)	$t_{SA/CO}$ = 55/20, 65/25	150 @ 65	Now
512K	64K x 8—Registered	28S	CY7C287	5962-92065(O)	$t_{SA/CO}$ = 55/20, 65/25	150 @ 65	Now
1M	128K x 8	32	CY27H010	5962-49614	t_{AA} = 35, 45, 55, 70	85@35	Now

PLDs

	Organization	Pins	Part Number	SMD Number[1]*	Speed (ns/MHz)	I_{CC} (mA @ ns/MHz)	883 Availability
PAL20	16L8, 16R8, 16R6, 16R4	20	PAL16XX	5962-92338(O)	t_{PD} = 7, 10	180 @ 7	Now
PALC20	16L8, 16R8, 16R6, 16R4	20	PALC16XX	5962-88678(W)	t_{PD} = 20, 30, 40	70 @ 20	Now
PALC20	16L8, 16R8, 16R6, 16R4	20	PALC16XX	5962-88713(O)	t_{PD} = 20, 30, 40	70 @ 20	Now
PALCE20	16V8—Macrocell	20S	PALCE16V8	5962-89839	$t_{PD/S/CO}$ = 10/10/7	130 @ 10	Now
PLD24	22V10C—Macrocell	24S	PAL22V10C	5962-91760(O)	$t_{PD/S/CO}$ = 10/3.6/7.5	190 @ 10	Now
PLD24	22V10C—Macrocell	24S	PAL22VP10C	5962-91760(O)	$t_{PD/S/CO}$ = 10/3.6/7.5	190 @ 10	Now
PLDC24	22V10—Macrocell	24S	PALC22V10	5962-87539(W)	$t_{PD/S/CO}$ = 25/18/15	100 @ 25	Now
PLD24	22V10—Macrocell	24S	PALC22V10B	5962-87539(W)	$t_{PD/S/CO}$ = 20/17/15	100 @ 20	Now
PLDC24	22V10—Macrocell	24S	PALC22V10	5962-88670(O)	$t_{PD/S/CO}$ = 25/18/15	100 @ 25	Now
PLD24	22V10—Macrocell	24S	PALC22V10B	5962-88670(O)	$t_{PD/S/CO}$ = 15/12/10	120 @ 15	Now
PLDC24	22V10—Macrocell	24S	PALC22V10B	M38510/507(W)	$t_{PD/S/CO}$ = 15/12/10	120 @ 15	Now
PLDC24	22V10—Macrocell	24S	PALC22V10B	M38510/508(O)	$t_{PD/S/CO}$ = 15/12/10	120 @ 15	Now
PLDC24	22V10D—Macrocell	24S	PALC22V10D	5962-89841(O)	$t_{PD/S/CO}$ = 10/6/7	130@10	Now
PLDC24	20G10—Generic	24S	PLDC20G10	5962-88637(O)	$t_{PD/S/CO}$ = 20/17/15	80 @ 30	Now
PLDC24	20RA10—Asynchronous	24S	PLD20RA10	5962-90555(O)	$t_{PD/SU/CO}$ = 20/10/20	100 @ 25	Now
PLDC24	20RA10—Asynchronous	24S	PLD20RA10	5962-90989(W)	$t_{PD/SU/CO}$ = 20/10/20	100@25	Now
PLDC28	7C330—State Machine	28S	CY7C330	5962-89546(W)	50, 40, 28 MHz	180 @ 40 MHz	Now
PLDC28	7C330—State Machine	28S	CY7C330	5926-90802(O)	50, 40, 28 MHz	180 @ 40 MHz	Now

PLDs (continued)

	Organization	Pins	Part Number	SMD Number[1]*	Speed (ns/MHz)	I_{CC} (mA @ ns/MHz)	883 Availability
PLDC28	7C331—Asynchronous	28S	CY7C331	5962-90754(W)	t_{PD} = 25, 30, 40	200 @ 20 MHz	Now
PLDC28	7C331—Asynchronous	28S	CY7C331	5962-89855(O)	t_{PD} = 25, 30, 40	200 @ 20 MHz	Now
PLDC28	7C332—Combinatorial	28S	CY7C332	5962-91584(W)	t_{PD} = 20, 25, 30	200 @ 24 MHz	Now
PLD28	7C335—Synchronous	28S	CY7C335	5862-94510(W)	f_{MAX5} = 66.6, 50, 83	160 @ 66.6 MHz	Now
MAX28	7C344—32 Macrocell	28S	CY7C344/B	5962-90611(W)	t_{PD} = 12, 20, 25, 35	220@25	Now
MAX40	7C343—64 Macrocell	40/44	CY7C343/B	5962-92158(W)	t_{PD} = 15, 20, 25, 30, 35	225@25	Now
MAX68	7C342—128 Macrocell	68	CY7C342/B	5962-89468(W)	t_{PD} = 15, 20, 25, 30, 35	320@30	Now
MAX84	7C341—192 Macrocell	84	CY7C341/B	5962-92062(W)	t_{PD} = 20, 25, 30, 35, 40	480@30	Now
MAX100	7C346—128 Macrocell	84/100	CY7C346/B	5962-93144(W)	t_{PD} = 20, 25, 30, 35	320@35	Now
37X−44	7C371—32 Macrocell	44	CY7C371	5962-94684(O)	$f_{MAX}/t_S/t_{CO}$=83MHz/ 10/10	260@83	Now
37X−44	7C372—64 Macrocell	44	CY7C372	5962-94688(O)	$f_{MAX}/t_S/t_{CO}$=83MHz/8/8	300@83	Now
37X−84	7C373—64 Macrocell	84	CY7C373	5962-94689(O)	$f_{MAX}/t_S/t_{CO}$=83MHz/8/8	300@83	Now
37X−84	7C374—128 Macrocell	84	CY7C374	5962-94713(O)	$f_{MAX}/t_S/t_{CO}$=83MHz/8/8	370@83	Now
37X−160	7C375—128 Macrocell	160	CY7C375	5962-95557	$f_{MAX}/t_S/t_{CO}$=83MHz/8/8	370@83	Now
FLASH370−240	7C377—192 Macrocell	240	CY7C377		$f_{MAX}/t_S/t_{CO}$=83MHz/ 12/12	300/TBD	2Q96
FLASH370−160	7C378—256 Macrocell	160	CY7C378		$f_{MAX}/t_S/t_{CO}$=83MHz/ 12/12	300/TBD	2Q96
FLASH370−240	7C379—256 Macrocell	240	CY7C379		$f_{MAX}/t_S/t_{CO}$=83MHz/ 12/12	300/TBD	2Q96
1K FPGA	CMOS 8 x 12	68	CY7C382A		−0, −1	20	Now
2K FPGA	CMOS 12 x 16	84	CY7C384A		−0, −1	20	Now
4K FPGA	CMOS 16 x 24	145	CY7C385A		−0, −1	20	Now
4K FPGA	CMOS 16 x 24	160	CY7C386A	5962-95599	−0, −1	20	Now
8K FPGA	CMOS 24x32	145/ 160/ 208	CY7C387A/8A		−0, −1	20	2Q96

FIFOs

Organization	Pins	Part Number	SMD Number	Speed	I_{CC}/I_{SB} (mA @ ns/MHz)	883 Availability
64 x 4—Cascadable	16	CY3341		1.2, 2 MHz	60 @ 2.0 MHz	Now
64 x 4—Cascadable	16	CY7C401	5962-89523	10, 15, 25 MHz	90 @ 15 MHz	Now
64 x 4—Cascadable/OE	16	CY7C403	5962-89523	10, 15, 25 MHz	90 @ 25 MHz	Now
64 x 5—Cascadable	18	CY7C402		10, 15, 25 MHz	90 @ 15 MHz	Now
64 x 5—Cascadable/OE	18	CY7C404	5962-86846	10, 15, 25 MHz	90 @ 25 MHz	Now
64 x 8—Cascadable/OE	28S	CY7C408A	5962-89664	15, 25 MHz	120 @ 25 MHz	Now
64 x 9—Cascadable	28S	CY7C409A	5962-89661	15, 25 MHz	120 @ 25 MHz	Now
512 x 9—Cascadable	28	CY7C420	5962-89863	t_A = 15, 20, 25, 30, 40, 65 ns	140/30 @ 30	Now
512 x 9—Cascadable	28S	CY7C421	5962-89863	t_A = 15, 20, 25, 30, 40, 65 ns	147/30 @ 25	Now
1K x 9—Cascadable	28	CY7C424	5962-91585	t_A = 15, 20, 25, 30, 40, 65 ns	140/30 @ 30	Now
1K x 9—Cascadable	28S	CY7C425	5962-91585	t_A = 15, 20, 25, 30, 40, 65 ns	147/30 @ 25	Now
2K x 9—Cascadable	28	CY7C428	5962-88669	t_A = 15, 20, 25, 30, 40, 65 ns	140/30 @ 30	Now
2K x 9—Cascadable	28S	CY7C429	5962-88669	t_A = 15, 20, 25, 30, 40, 65 ns	147/30 @ 25	Now
2K x 9—Bidirectional	28S	CY7C439	5962-92321	t_A = 15, 20, 25, 30, 40, 65 ns	170/45 @ 30	Now
4K x 9—Cascadable	28	CY7C432	5962-90715	t_A = 15, 20, 25, 30, 40, 65 ns	160/30 @ 30	Now
4K x 9—Cascadable	28S	CY7C433	5962-90715	t_A = 15, 20, 25, 30, 40, 65 ns	160/30 @ 30	Now
512 x 9—Clocked	28S	CY7C441	5962-95568	t_C = 14, 20, 30 ns	160 @ 14	Now
2K x 9—Clocked	28S	CY7C443	5962-94567	t_C = 14, 20, 30 ns	160 @ 14	Now
512 x 9—Clocked/Cascadable	32	CY7C451	5962-93173	t_C = 14, 20, 30 ns	160 @ 14	Now
2K x 9—Clocked/Cascadable	32	CY7C453	5962-93124	t_C = 14, 20, 30 ns	160 @ 14	Now
8K x 9—Half Full Flag	28	CY7C460		t_A = 20, 25, 40 ns	110 @ 20	Now
8K x 9—Prog. Flags	28	CY7C470		t_A = 20, 25, 40 ns	110 @ 20	Now
16K x 9—Half Full Flag	28	CY7C462	5962-93008	t_A = 20, 25, 40 ns	110 @ 20	Now

FIFOs (continued)

Organization	Pins	Part Number	SMD Number	Speed	I_{CC}/I_{SB} (mA @ ns/MHz)	883 Availability
16K x 9—Prog. Flags	28	CY7C472		t_A = 20, 25, 40 ns	110 @ 20	Now
32K x 9—Half Full Flag	28	CY7C464	5962-93152	t_A = 20, 25, 40 ns	110 @ 20	Now
32K x 9—Prog. Flags	28	CY7C474	5962-94588	t_A = 20, 25, 40 ns	110 @ 20	Now

Logic

Organization	Pins	Part Number	SMD Number	Speed (ns)	I_{CC} (mA @ ns)	883 Availability
Programmable Skew Clock Buffer (TTL Outputs)	32	CY7B991	5962-94522	f_{REF} = 15 – 80 MHz	75	Now
Programmable Skew Clock Buffer (CMOS Outputs)	32	CY7B992	5962-93112	f_{REF} = 15 – 80 MHz	75	Now
2901—4-Bit Slice	40	CY7C901	5962-88535	t_{CLK} = 27, 32	90 @ 27	Now
2901—4-Bit Slice	40	CY2901C	5962-88535	C	180 @ 32	Now
4 x 2901—16-Bit Slice	64	CY7C9101	5962-89517	t_{CLK} = 35, 45	85 @ 35	Now
2911—Sequencer	20	CY7C911	5962-90609	t_{CLK} = 30, 40	55 @ 30	Now
2910—Controller (17-Word Stack)	40	CY7C910	5962-87708	t_{CLK} = 46, 51, 99	90 @ 46	Now
2910—Controller (9-Word Stack)	40	CY2910A	5962-87708	A	170 @ 51	Now

VMEbus Interface Products

Organization	Pins	Part Number	SMD Number	Speed (MHz)	I_{CC} (mA)	883 Availability
VME Interface Controller	144/160	VIC068A	5962-92010	64	250	Now
VME Address Controller	144/160	VAC068A	5962-92009	50	150	Now
64-Bit VIC	144/160	VIC64		64	300	Now
Slave VME Interface Controller	64	CY7C960				Now
Bus Interface Logic Circuit	64	CY7C964	5962-95511			Now

Communication Products

Organization	Pins	Part Number	Speed (Mbps)	I_{CC} (mA)	Packages	883 Availability
HOTLink Transmitter	28	CY7B923	160 – 330	95	L	Now
HOTLink Receiver	28	CY7B933	160 – 330	165	L	Now

Modules

Size	Organization	Pins	Part Number	Packages	Speed (ns)	I_{CC} (mA @ ns)	883 Availability
1M	32K x 32 SRAM	66	CYM1828	HG01	t_{AA} = 35, 45, 55, 70	200@35	Now
2M	64K x 32 SRAM	60	CYM1830	HD06	t_{AA} = 35, 45, 55	880 @ 35	Now
4M	128K x 32 SRAM	66	CYM1838	HG01	t_{AA} = 25, 30, 35	720 @ 25	Now
4M	512K x 8 SRAM	32	CYM1466	HD12	t_{AA} = 35, 45, 55, 70, 85, 100, 120	350 @ 35	Now

Notes:

The following Cypress facilites have been granted Level Q (QML) transitional certification by DESC:

Operation	Facility	Location
Fab	Fab2	Round Rock, TX
	Fab3	Bloomington, MN
Assy/Test	Bangkok	Bangkok, Thailand
Test	San Jose	San Jose, CA

All of the above products are available with processing to MIL-STD-883D at a minimum. Many of these products are also available to SMDs (Standardized Military Drawings).

The speed and power specifications listed above cover the full military temperature range.

Modules are available with MIL-STD-883D components. These modules are assembled and screened to the proposed JEDEC military processing standard for modules.

W = Windowed Package
O = Opaque Package
HD = Hermetic DIP Module

22S stands for 22-pin 300-mil DIP.
24S stands for 24-pin 300-mil DIP.
28S stands for 28-pin 300-mil DIP.
32S stands for 32-pin 300-mil DIP.