

Cypress Semiconductor CD-ROM

Product Press Releases

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Product Press Releases		
<u>PR01</u>	Cypress Delivers USB 2.0 Mass Storage Reference Design Kit	06/19/01
<u>PR02</u>	Cypress Introduces Programmable InfiniBand PHY	06/18/01
<u>PR03</u>	QDR Co-Development Team Announces Next-Generation Product Architectures	06/14/01
<u>PR04</u>	Cypress Announces Synplicity Support For Programmable Physical Layer Devices	06/08/01
<u>PR05</u>	Cypress Delivers Samples of 4 Mbit Dual-Port Memory	05/23/01
<u>PR06</u>	Cypress Consumer Clock Family is First to Target DVD, MiniDisc, and Digital Still Cameras	05/22/01
<u>PR07</u>	Cypress <i>Warp</i> Software Adds Support for Programmable Serial Interface (PSI) Devices	05/16/01
<u>PR08</u>	Eureka Delivers PowerPC IP Cores to Cypress's IP Oasis Program	05/21/01
<u>PR09</u>	Cypress Transceiver for High-Speed Communications Backplanes Expands HOTLink Portfolio	05/14/01
<u>PR10</u>	Cypress Samples World's First Programmable SONET/SDH OC-48 PHY	05/04/01
<u>PR11</u>	ComL Devices from Cypress are Industry's Fastest Bus Interface and Backplane Chips	04/24/01
<u>PR12</u>	Cypress Ships 10-Gbps 3.3V FIFOs	04/20/01
<u>PR13</u>	Cypress Semiconductor and Agilent Technologies to Develop Optical Mouse Technology	04/16/01
<u>PR14</u>	Eureka Delivers IP Cores to Cypress's Delta39K CPLDs	04/04/01
<u>PR15</u>	Cypress Samples World's First 2.5 Gbps Programmable PHY	04/03/01
<u>PR16</u>	Samsung Joins QDR Co-Development Team to Produce Quad Data Rate SRAMs	04/02/01
<u>PR17</u>	Cypress Samples 9 Mbit QDR Memories	03/30/01
<u>PR18</u>	Carry Computer Launches USB Flash Card Reader with Cypress USB 2.0 Chip at CeBIT	03/27/01
<u>PR19</u>	Cypress Samples 18 Mbit Sync SRAM Family	03/23/01
<u>PR20</u>	Cypress Ships High-Bandwidth QuadPort RAM	03/21/01
<u>PR21</u>	Cypress Ships High-Performance Alternative to Popular Clock Buffer	03/16/01
<u>PR22</u>	Cypress Intelligent Serial Interface Engine Provides USB 2.0 Connectivity to Embedded Microcontrollers	02/19/01
<u>PR23</u>	Cypress Highlights Integrated USB 2.0 Solution and Timing Technology at Intel Forum	02/16/01
<u>PR24</u>	Cypress's In-System Reprogrammable Software Adds Support for Delta39K CPLDs	02/09/01
<u>PR25</u>	Cypress Introduces Industry's First Integrated USB 2.0 Peripheral Controller	01/29/01
<u>PR26</u>	Cypress Samples Industry's First 9-Mbit Dual-Port Communications RAM	01/24/01
<u>PR27</u>	Cypress Ships 200-MHz RoboClock II Programmable Skew Clock Buffer	01/15/01
<u>PR28</u>	Cypress Programmable Timing Technology Adopted by Sony for Use In Playstation2 Computer Entertainment System	12/22/00
<u>PR29</u>	Cypress Launches High-Speed Programmable Communications ICs	12/18/00
<u>PR30</u>	Cypress Timing Technology Enconuters High Demand In Communications Market	12/01/00

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PR31	Cypress MicroSystems Unveils Programmable System-on-Chip For Embedded Internet, Communications, and Consumer Systems	11/13/00
PR32	Cypress Samples World's Largest CPLDs	11/06/00
PR33	Cypress's 2.5 Gbps InfiniBand Transceiver For Switch Fabric Architecture Targets High-Speed Data Networks, Storage Networks, and Communications Backplanes	10/23/00
PR34	Cypress Introduces the First 40-MHz Communications Processor For LonWorks' EIA-709.1 Control Networks	10/16/00
PR35	Cypress Unveils Blueprint to Drive the Communications Revolution	10/16/00
PR36	Cypress Licenses Make-Link Laser Technology From LaserLink	10/13/00
PR37	Cypress Launches IP Oasis Support Site to Accelerate Time-to-Market of Communications	10/10/00
PR38	Cypress 4 Mbit Dual Port Memory Provides Density Upgrade for High-End Storage Networks,	09/25/00
PR39	Cypress Expands Family of PREMIS Frequency Timing Generators, Using Advanced Spread-Spectrum	08/23/00
PR40	Cypress's 480 Mbps USB 2.0 Peripheral Controller Is First To Demonstrate Functionality at Intel Laboratory	08/22/00
PR41	Cypress OC-48 Transceiver With Integrated CDR Lowers Real Estate and Power Requirements on System Boards	08/21/00
PR42	Cypress Extends Networking SRAM Portfolio to 18 Mbit Density	08/11/00
PR43	Cypress Announces 0.15-micron Technology	08/07/00
PR44	Cypress Announces Synplicity Support For Delta39K CPLDs	08/04/00
PR45	Cypress Introduces Industry's First 533-MHz Direct RAMBUS Clock Generator	07/31/00
PR46	Cypress Extends Networking SRAM Portfolio To 9 Mbit Density	07/19/00
PR47	Cypress's <i>Warp</i> Leads Programmable Logic Into a New Era of HDL Design	07/17/00
PR50	Cypress Launches Integrated Dual-Port-PCI Bus Controller	06/28/00
PR51	Mentor Graphics and Cypress Semiconductor Announce Web-Based Delivery of Optimized IP Solutions	06/12/00
PR52	Cypress Launches Next-Generation USB Mouse Controllers	06/12/00
PR53	Cypress Completes Acquisition of RF Specialist Alation Systems	06/07/00
PR54	Cypress Unveils USB Keyboard-Hub Evaluation Kit	05/30/00
PR55	Cypress Warp Software Shipments Eclipse 25,000 Mark	05/26/00
PR56	Cypress Tops 20 Million Mark in Zero Delay Buffer Shipments	05/15/00
PR57	Cypress Acquires RF Specialist Alation Systems; Move Geared to Achieve Critical Mass in Wireless Business	05/03/01
PR58	Cypress Boosts Performance of FLEx36 Dual-Ports	05/01/00
PR59	Cypress to Acquire RF Circuit Specialist Radiocom Corp.	04/27/01
PR60	Cypress Extends Aldec Support to Next-Generation Warp Software	04/26/00
PR61	Cypress to Support Very-High-Speed DSPs with x24 Memory Product	04/19/00
PR62	Cypress Announces Industry's Highest-Performance USB Microcontroller	04/17/00

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<u>PR63</u>	Cypress Sets the New Bandwidth Standard in Specialty Memories	04/10/00
<u>PR64</u>	Cypress, Fujitsu, inSilicon, Intel, HP, Kawasaki LSI, Lucent, ST Microelectronics, Texas Instruments Back New USB 2.0 Transceiver Interface	03/30/00
<u>PR65</u>	Cypress Announces Doubling of Shipments of Mapper/Framer Silicon	03/29/01
<u>PR66</u>	Cypress Hits 50 Million Mark in USB Shipments	03/24/00
<u>PR67</u>	Cypress Debuts RoboClock II Programmable Clock Buffer for Next-Generation Communications Applications	03/20/00
<u>PR68</u>	Cypress Announces Silicon on biCMOS Process With World-Class Speed, Power Characteristics	03/13/01
<u>PR70</u>	Cypress, Synplicity Offer Promotional Synplify Software for Ultra 37000 CPLDs	03/01/00
<u>PR71</u>	Cypress and Hyundai Microelectronics Sign Manufacturing Agreement	02/23/00
<u>PR72</u>	Xilinx Announces Industry's First Memory Controller Solution For Quad Data Rate SRAMs	02/22/01
<u>PR73</u>	Cypress Launches RF Business with Lowest-Phase-Noise PLL Family	02/21/00
<u>PR74</u>	QDR SRAM Consortium Releases Specifications	02/16/00
<u>PR75</u>	Cypress Builds Specialized Test And Assembly Line For FBGA Packages	02/15/01
<u>PR76</u>	Cypress And ADT Sign Joint R&D Agreement	02/09/00
<u>PR77</u>	Cypress Semiconductor To Acquire Galvantech, Inc.	01/11/00
<u>PR78</u>	QDR RAM Design Complete; Silicon Moves Into Manufacturing	01/10/00