



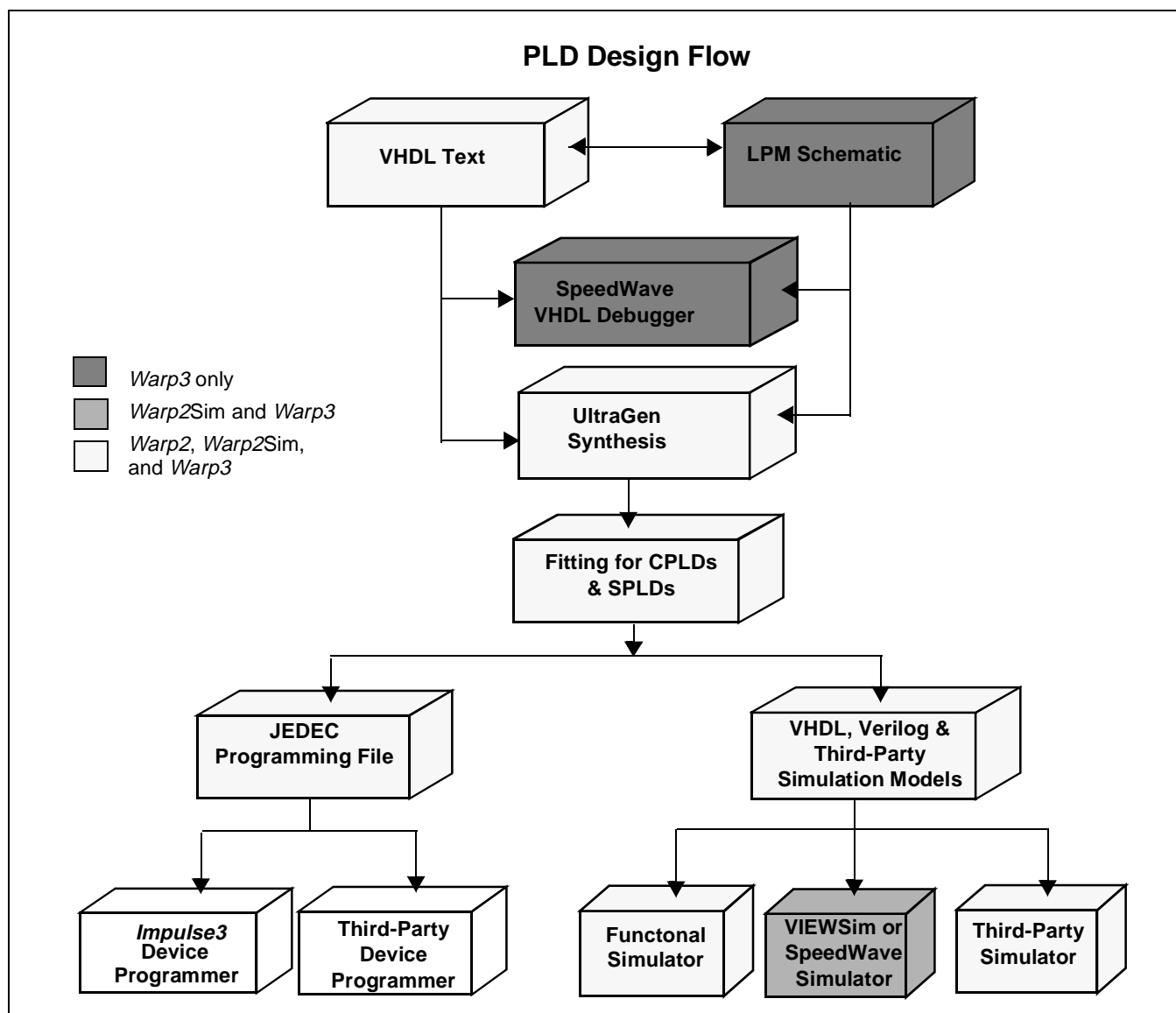
PLD Development Tools Overview

A large number of development tools are available for use when designing with Cypress Semiconductor's PLDs and CPLDs. Many of these tools are available from Cypress, while additional design flow options are available from numerous third-party tool vendors. (For a complete listing of third-party tool vendors, see the Third-Party Tools datasheet.)

Development software is available that provides design entry, synthesis, optimization, fitting, and simulation. As shown below, this software produces a programming file for use with a device programmer. Cypress offers the *Warp* software family of industry-leading VHDL synthesis tools. *Warp2*[®] provides VHDL design description and functional simulation.

Warp2Sim[™] has all of the features of *Warp2* and adds full timing simulation. *Warp3*[®] includes *Warp2Sim* functionality plus schematic entry and VHDL source code simulation. In addition, *Warp* seamlessly bolts-in to many third-party tools for various levels of support.

Device programmers use the programming file created by the development tool to program the PLD or CPLD. The *Impulse3*[™] can program any Cypress Programmable Logic device and can be upgraded to program other manufacturers' devices. Many third-party programmers are available that can be used to program a wide array of devices including those from Cypress.



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