



4.2 Features

- Directly connects to VIC068A, VIC64, CY7C960, or CY7C961
- Has internal counters for block transfer and local DMA address control
- Has internal multiplexers for D64 64-bit VMEbus block transfers
- Has internal comparators for address decoding
- Supports VIC068A/VIC64 dual-address-path option
- Has cascadable operation
- Directly drives VMEbus address and data bus signals
- Directly drives local address and data bus signals
- Reduces components for a compact board design
- Has low power requirements
- Is available in a 64-pin TQFP or 68-pin PGA