



5.8

DC Performance Specifications

Table 5-6. VMEbus Signals (AS*, DS1*, DS0*, BCLR*, SYSCLK)

Parameter	Description	Test Conditions		Comm.	Industrial	Military	Units
V_{IH}	Minimum High-Level Input Voltage			2.0	2.0	2.0	V
V_{IL}	Maximum Low-Level Input Voltage			0.8	0.8	0.8	V
V_{OH}	Minimum High-Level Output Voltage	$V_{CC} = \text{Min.}$, $I_{OH} = -3 \text{ mA}$		2.4	2.4	2.4	V
V_{OL}	Maximum Low-Level Output Voltage	$V_{CC} = \text{Min.}$, $I_{OL} = 48 \text{ mA}, 56 \text{ mA}, 64 \text{ mA}$		0.6	0.6	0.6	V
I_L	Maximum Input Leakage Current	$V_{CC} = \text{Max.}$, $V_{IN} = 0.6\text{--}2.4$		± 5	± 5	± 5	μA
V_{IK}	Input Clamp Voltage	$V_{CC} = \text{Min.}$	$I_{IN} = -18 \text{ mA}$	-1.2	-1.2	-1.2	V
V_{IK}	Input Clamp Voltage	$V_{CC} = \text{Min.}$	$I_{IN} = 18 \text{ mA}$	$V_{CC}+1.2$	$V_{CC}+1.2$	$V_{CC}+1.2$	V
I_{OZ}	Maximum Output Leakage Current	$V_{CC} = \text{Max.}$, $\text{GND} \leq V_{OUT} \leq V_{CC}$ All Outputs Disabled		± 10	± 10	± 10	μA

Table 5-7. VMEbus Signals (Low Drive. All VMEbus Daisy-Chain Signals.)

Parameter	Description	Test Conditions		Comm.	Industrial	Military	Units
V_{IH}	Minimum High-Level Input Voltage			2.0	2.0	2.0	V
V_{IL}	Maximum Low-Level Input Voltage			0.8	0.8	0.8	V
V_{OH}	Minimum High-Level Output Voltage	$V_{CC} = \text{Min.}$, $I_{OH} = -8 \text{ mA}$		2.4	2.4	2.4	V
V_{OL}	Maximum Low-Level Output Voltage	$V_{CC} = \text{Min.}$, $I_{OL} = 8 \text{ mA}$		0.6	0.6	0.6	V
I_L	Maximum Input Leakage Current	$V_{CC} = \text{Max.}$, $V_{IN} = 0.6\text{--}2.4$		± 5	± 5	± 5	μA
V_{IK}	Input Clamp Voltage	$V_{CC} = \text{Min.}$	$I_{IN} = -18 \text{ mA}$	-1.2	-1.2	-1.2	V
V_{IK}	Input Clamp Voltage	$V_{CC} = \text{Min.}$	$I_{IN} = 18 \text{ mA}$	$V_{CC}+1.2$	$V_{CC}+1.2$	$V_{CC}+1.2$	V
I_{OZ}	Maximum Output Leakage Current	$V_{CC} = \text{Max.}$, $V_{OUT} = 0.6/2.4\text{V}$ All Outputs Disabled		± 5	± 5	± 10	μA

Table 5-8. VMEbus Signals (Medium Drive. All non-High, non-Low Drive Signals, All VAC068A VMEbus Signals.)

Parameter	Description	Test Conditions		Comm.	Industrial	Military	Units
V_{IH}	Minimum High-Level Input Voltage			2.0	2.0	2.0	V
V_{IL}	Maximum Low-Level Input Voltage			0.8	0.8	0.8	V
V_{OH}	Minimum High-Level Output Voltage	$V_{CC} = \text{Min.}, I_{OH} = -3 \text{ mA}$		2.4	2.4	2.4	V
V_{OL}	Maximum Low-Level Output Voltage	$V_{CC} = \text{Min.}, I_{OL} = 48 \text{ mA}$		0.6	0.6	0.6	V
I_L	Maximum Input Leakage Current	$V_{CC} = \text{Max.}, V_{IN} = 0.6\text{--}2.4$		± 5	± 5	± 5	μA
V_{IK}	Input Clamp Voltage	$V_{CC} = \text{Min.}$	$I_{IN} = -18 \text{ mA}$	-1.2	-1.2	-1.2	V
V_{IK}	Input Clamp Voltage	$V_{CC} = \text{Min.}$	$I_{IN} = 18 \text{ mA}$	$V_{CC} + 1.2$	$V_{CC} + 1.2$	$V_{CC} + 1.2$	V
I_{OZ}	Maximum Output Leakage Current	$V_{CC} = \text{Max.}, V_{OUT} = 0.6/2.4\text{V}$ All Outputs Disabled		± 5	± 5	± 10	μA

Table 5-9. Non-VMEbus Signals

Parameter	Description	Test Conditions		Comm.	Industrial	Military	Units
V_{IH}	Minimum High-Level Input Voltage			2.0	2.0	2.0	V
V_{IL}	Maximum Low-Level Input Voltage			0.8	0.8	0.8	V
V_{OH}	Minimum High-Level Output Voltage	$V_{CC} = \text{Min.}$, $I_{OH} = -8 \text{ mA}$		2.4	2.4	2.4	V
V_{OL}	Maximum Low-Level Output Voltage	$V_{CC} = \text{Min.}$, $I_{OL} = 8 \text{ mA}$		0.6	0.6	0.6	V
I_L	Maximum Input Leakage Current	$V_{CC} = \text{Max.}$, $V_{IN} = 0.00/V_{CC}$		± 5	± 5	± 5	μA
V_{IK}	Input Clamp Voltage	$V_{CC} = \text{Min.}$	$I_{IN} = -18 \text{ mA}$	-1.2	-1.2	-1.2	V
V_{IK}	Input Clamp Voltage	$V_{CC} = \text{Min.}$	$I_{IN} = 18 \text{ mA}$	$V_{CC} + 1.2$	$V_{CC} + 1.2$	$V_{CC} + 1.2$	V
I_{OZ}	Maximum Output Leakage Current	$V_{CC} = \text{Max.}$, $GND \leq V_{OUT} \leq V_{CC}$ All Outputs Disabled		± 5		± 10	μA

Table 5-10. Capacitance

Parameters	Description	Test Conditions	Max.	Units
C_{IN}	Input Capacitance	$T_A = 25^\circ\text{C}$, $f = 64 \text{ MHz}$, $V_{CC} = 5.0\text{V}$	5	pF
C_{OUT}	Output Capacitance		7	pF

Table 5-11. Operating Current

Parameters	Description	Test Conditions	Max.	Units
I_{DD}	Maximum Operating Current	No external DC load	150	mA