

# Cypress Semiconductor CD-ROM

## Static RAMs

Part Number	Part Description	Last Updated
<b>Quad Data Rate™ (QDR™) SRAMs</b>		
<a href="#">CY7C1302V25</a>	9 Mb Pipelined SRAM with QDR™ Architecture	03/30/00
<a href="#">CY7C1303V25</a>	18 Mb Burst of 2 Pipelined SRAM with QDR Architecture	07/03/01
<a href="#">CY7C1304V25</a>	9 Mb Pipelined SRAM with QDR Architecture	02/15/00
<a href="#">CY7C1305V25</a>	18 Mb Burst of 4 Pipelined SRAM with QDR Architecture	07/03/01
<a href="#">CY7C1306V25</a>	18 Mb Burst of 2 Pipelined SRAM with QDR Architecture	07/03/01
<a href="#">CY7C1307V25</a>	18 Mb Burst of 4 Pipelined SRAM with QDR Architecture	07/03/01
<b>NetRAMs™</b>		
<a href="#">CY7C1300A</a>	128K x 36 Dual I/O Dual Address Synchronous SRAM	06/07/01
<a href="#">CY7C1301A</a>	256K x 36 Dual I/O Dual Address Synchronous SRAM	06/07/01
<b>NoBL™ (No Bus Latency™) SRAMs</b>		
<a href="#">CY7C1333</a>	64K x 32 Flow-Through SRAM with NoBL™ Architecture	08/05/99
<a href="#">CY7C1334</a>	64K x 32 Pipelined SRAM with NoBL Architecture	08/09/99
<a href="#">CY7C1350</a>	128K x 36 Pipelined SRAM with NoBL Architecture	08/09/99
<a href="#">CY7C1350B</a>	128K x 36 Pipelined SRAM with NoBL Architecture	03/16/01
<a href="#">CY7C1351</a>	128Kx 36 Flow-Through SRAM with NoBL Architecture	08/10/99
<a href="#">CY7C1351B</a>	128Kx 36 Flow-Through SRAM with NoBL Architecture	07/18/00
<a href="#">CY7C1352</a>	256K x 18 Pipelined SRAM with NoBL Architecture	03/14/01
<a href="#">CY7C1352B</a>	256K x 18 Pipelined SRAM with NoBL Architecture	05/30/00
<a href="#">CY7C1353</a>	256K x 18 Flow-Through SRAM with NoBL Architecture	08/09/99
<a href="#">CY7C1353B</a>	256K x 18 Flow-Through SRAM with NoBL Architecture	04/04/00
<a href="#">CY7C1354A / GVT1256ZC36</a>	256K x 36/512K x 18 Pipelined SRAM with NoBL Architecture	05/19/00
<a href="#">CY7C1354V25</a>	256K x 36 Pipelined SRAM with NoBL Architecture	12/06/99
<a href="#">CY7C1355A / GVT1256ZB36</a>	256K x 36/512K x 18 Flowthrough SRAM with NoBL Architecture	05/24/01
<a href="#">CY7C1356A / GVT71512ZC18</a>	256K x 36/512Kx18 Pipelined SRAM with NoBL Architecture	05/19/00
<a href="#">CY7C1356V25</a>	512K x 18 Pipelined SRAM with NoBL Architecture	12/06/99
<a href="#">CY7C1357A / GVT71512ZB18</a>	256K x 36 Flowthrough SRAM with NoBL Architecture	05/24/01
<a href="#">CY7C1370B</a>	512K x 36 / 1 Mb x 18 Pipelined SRAM with NoBL Architecture	06/27/01
<a href="#">CY7C1370BV25</a>	512K x 36 / 1 Mb x 18 Flowthrough SRAM with NoBL Architecture	06/27/01
<a href="#">CY7C1371B</a>	512K x 36/1Mx18 Flowthrough SRAM with NoBL Architecture	06/27/01
<a href="#">CY7C1371BV25</a>	512K x 36/1Mx18 Flowthrough SRAM with NoBL Architecture	06/27/01

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<a href="#">CY7C1372B</a>	512K x 36/1Mx18 Pipelined SRAM with NoBL Architecture	06/27/01
<a href="#">CY7C1372BV25</a>	512K x 36 / 1Mb x 18 Pipelined SRAM with NoBL Architecture	06/27/01
<a href="#">CY7C1373B</a>	512K x 36/1Mx18 Flowthrough SRAM with NoBL Architecture	06/27/01
<a href="#">CY7C1373BV25</a>	512K x 36/1Mx18 Flowthrough SRAM with NoBL Architecture	06/27/01
<b>Galvantech</b>		
<a href="#">GVT71128ZB36</a>	128K x 36 Flowthrough with Zero Bus Latency	02/01/99
<a href="#">GVT71128ZC36</a>	128K x 36 Pipeline with Zero Bus Latency	02/01/99
<a href="#">GVT71256ZB18</a>	256K x 18 Flow-Through with Zero Bus Latency	02/01/99
<a href="#">GVT71256ZC18</a>	256K x 18 Pipeline with Zero Bus Latency	02/01/99
<b>Synchronous SRAMs</b>		
<a href="#">CY7C1031</a>	64K x 18 Synchronous Cache RAM	11/30/99
<a href="#">CY7C1032</a>	64K x 18 Synchronous Cache RAM	11/30/99
<a href="#">CY7C1324</a>	3.3V 128K x 18 Synchronous Cache RAM	08/05/99
<a href="#">CY7C1325</a>	256K x 18 Synchronous 3.3V Cache RAM	05/10/00
<a href="#">CY7C1325B</a>	256K x 18 Synchronous 3.3V Cache RAM	09/07/00
<a href="#">CY7C1327</a>	256Kx18 Synchronous-Pipelined Cache RAM	05/09/00
<a href="#">CY7C1327B</a>	256Kx18 Synchronous-Pipelined Cache RAM	05/09/00
<a href="#">CY7C1327C / GVT71256DA18</a>	256K x 18 Synchronous-Pipelined Cache RAM	07/24/00
<a href="#">CY7C1328A / GVT1256F18</a>	256K x 18 Synchronous-Pipelined Cache RAM	06/15/00
<a href="#">CY7C1329</a>	64K x 32 Synchronous-Pipelined Cache RAM	08/09/99
<a href="#">CY7C1335</a>	32K x 32 Synchronous Cache RAM	07/30/99
<a href="#">CY7C1338</a>	128Kx32 Synchronous Flow-Through 3.3V Cache RAM	05/09/00
<a href="#">CY7C1338B</a>	128Kx32 Synchronous Flow-Through 3.3V Cache RAM	01/18/01
<a href="#">CY7C1339</a>	128Kx32 Synchronous-Pipelined Cache RAM	05/08/00
<a href="#">CY7C1339B</a>	128Kx32 Synchronous-Pipelined Cache RAM	01/18/01
<a href="#">CY7C1340A / GVT71128C32</a>	128Kx32 Synchronous-Pipelined RAM	06/15/00
<a href="#">CY7C1344A / GVT7164B36</a>	64Kx36 Synchronous Burst RAM	06/15/00
<a href="#">CY7C1345</a>	128Kx36 Synchronous Flow-Through 3.3V Cache RAM	05/08/00
<a href="#">CY7C1345B</a>	128Kx36 Synchronous Flow-Through 3.3V Cache RAM	09/11/00
<a href="#">CY7C1346A / GVT7164D36</a>	64Kx36 Synchronous Pipelined Burst SRAM	06/15/00
<a href="#">CY7C1347</a>	128Kx36 Synchronous-Pipelined Cache RAM	05/09/00

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<a href="#">CY7C1347B</a>	128Kx36 Synchronous-Pipelined Cache RAM	03/11/01
<a href="#">CY7C1347C / GVT71128DA36</a>	128Kx36 Synchronous-Pipelined Cache RAM	07/24/00
<a href="#">CY7C1348A / GVT71128F36</a>	128K x 36 Synchronous-Pipelined Cache RAM	07/14/00
<a href="#">CY7C1360A / GVT71256D36</a>	256K x 36/512K x 18 Pipelined SRAM	05/09/01
<a href="#">CY7C1360A1 / GVT71256DA36</a>	256K x 36/512K x 18 Pipelined SRAM	05/19/00
<a href="#">CY7C1361A / GVT71256B36</a>	256K x 36/512K x 18 Synchronous Burst Flowthrough SRAM	06/11/01
<a href="#">CY7C1362A / GVT71512D18</a>	256K x 36/512K x 18 Pipelined SRAM	05/19/00
<a href="#">CY7C1362A1 / GVT71512D18</a>	256K x 36/512K x 18 Pipelined SRAM	05/19/00
<a href="#">CY7C1363A / GVT71512B18</a>	256K x 36/512K x 18 Synchronous Burst Flowthrough SRAM	06/11/01
<a href="#">CY7C1364V25</a>	256K x 32 Pipelined SRAM	10/20/00
<a href="#">CY7C1366A / GVT71256C36</a>	256K x 36/512K x 18 Pipelined SRAM	06/12/01
<a href="#">CY7C1367A / GVT71512C18</a>	256K x 36/512K x 18 Pipelined SRAM	06/12/01
<a href="#">CY7C1380B</a>	512K x 36/1M x 18 Pipelined SRAM	07/02/01
<a href="#">CY7C1380BV25</a>	512K x 36/1M x 18 Pipelined SRAM	07/05/01
<a href="#">CY7C1381B</a>	512K x 36/1M x 18 Flow-Through SRAM	07/02/01
<a href="#">CY7C1381BV25</a>	512K x 36/1M x 18 Flow-Through SRAM	07/02/01
<a href="#">CY7C1382B</a>	512K x 36/1M x 18 Pipelined SRAM	07/02/01
<a href="#">CY7C1382BV25</a>	512K x 36/1M x 18 Pipelined SRAM	07/05/01
<a href="#">CY7C1383B</a>	512K x 36/1M x 18 Flow-Through SRAM	07/02/01
<a href="#">CY7C1383BV25</a>	512K x 36/1M x 18 Flow-Through SRAM	07/20/01
<a href="#">CY7C1386B</a>	512K x 36 / 1 Mb x 18 Pipelined SRAM	07/05/01
<a href="#">CY7C1386BV25</a>	512K x 36 / 1 Mb x 18 Pipelined SRAM	07/02/01
<a href="#">CY7C1387B</a>	512K x 36 / 1 Mb x 18 Pipelined SRAM	07/05/01
<a href="#">CY7C1387BV25</a>	512K x 36 / 1 Mb x 18 Pipelined SRAM	07/02/01
<b>Galvantech</b>		
<a href="#">GVT7132B36</a>	32K x 36 Flow-Through SRAM	02/01/98
<a href="#">GVT7132C32</a>	32K x 32 Pipelined SRAM	06/01/96
<a href="#">GVT7132D32</a>	32K x 32 Pipelined SRAM	04/01/99
<a href="#">GVT7164B18</a>	64K x 18 Flow-Through SRAM	02/01/98
<a href="#">GVT7164B19</a>	64K x 18 Flow-Through SRAM	12/01/97
<a href="#">GVT7164C18</a>	64K x 18 Pipelined SRAM	02/01/98
<a href="#">GVT7164D18</a>	64K x 18 Pipelined SRAM	02/01/98

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Part Number	Part Description	Last Updated
<a href="#">GVT7164T18</a>	64K x 18 Tag RAM	11/01/97
<a href="#">GVT71128B18</a>	128K x 18 Flow-Through SRAM	02/01/99
<a href="#">GVT71128D18</a>	128K x 18 Pipelined SRAM	10/01/99
<a href="#">GVT7164D32</a>	64K x 32 Pipelined SRAM	03/01/99
<a href="#">GVT71128B32</a>	128K x 32 Flow-Through SRAM	01/01/99
<a href="#">GVT71128D32</a>	128K x 32 Pipelined SRAM	11/01/99
<a href="#">GVT71128E36</a>	128K x 36 Flow-Through SRAM	05/01/98
<a href="#">GVT71128G36</a>	128K x 36 Pipelined SRAM	05/01/98
<a href="#">GVT71256D18</a>	256K x 18 Pipelined SRAM	11/01/98
<a href="#">GVT71256E18</a>	256K x 18 Flow-Through SRAM	05/01/98
<a href="#">GVT71256G18</a>	256K x 18 Pipelined SRAM	05/01/98
<a href="#">GVT71256T18</a>	256K x 18 Pipelined SRAM	08/01/98
<a href="#">GVT7164D64</a>	64K x 64 Pipelined SRAM	03/20/98

#### Fast Asynchronous SRAMs

<a href="#">CY7C1006</a>	256K x 4 Static RAM	07/06/98
<a href="#">CY7C1006B</a>	256K x 4 Static RAM	03/23/01
<a href="#">CY7C107 / CY7C1007</a>	1M x 1 Static RAM	07/06/98
<a href="#">CY7C107B / CY7C1007B</a>	1Mb x 1 Static RAM	05/24/01
<a href="#">CY7C1009</a>	128K x 8 Static RAM	09/10/99
<a href="#">CY7C1009B</a>	128K x 8 Static RAM	03/23/01
<a href="#">CY7C1019</a>	128K x 8 Static RAM	04/06/98
<a href="#">CY7C1019B / CY7C10191B</a>	128K x 8 Static RAM	05/17/01
<a href="#">CY7C1019V33 / CY7C1019BV33</a>	128K x 8 Static RAM	10/19/99
<a href="#">CY7C1011BV33</a>	128K x 16 Static RAM	06/06/01
<a href="#">CY7C1020</a>	32K x 16 Static RAM	10/18/99
<a href="#">CY7C1020V</a>	32K x 16, 3.3V Static RAM	04/14/98
<a href="#">CY7C1021</a>	64K x 16 Static RAM	07/20/00
<a href="#">CY7C1021B / CY7C10211B</a>	64K x 16 Static RAM	05/17/01
<a href="#">CY7C1021V / CY7C1021BV33</a>	64K x 16, 3.3V Static RAM	10/18/99
<a href="#">CY7C1022</a>	32K x 16 Static RAM	04/06/98
<a href="#">CY7C1024AV33</a>	128K x 4 Static RAM	04/06/01
<a href="#">CY7C1041</a>	256K x 16 Static RAM	10/05/99

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Part Number	Part Description	Last Updated
<a href="#">CY7C1041B</a>	256K x 16 Static RAM	03/23/01
<a href="#">CY7C1041BV33</a>	256K x 16 Static RAM	07/07/00
<a href="#">CY7C1041V33</a>	256K x 16 Static RAM	06/02/99
<a href="#">CY7C1046 / CY7C1046B</a>	1M x 4 Static RAM	06/15/99
<a href="#">CY7C1046BV33</a>	1M x 4 Static RAM	04/05/00
<a href="#">CY7C1049 / CY7C1049B</a>	512K x 8 Static RAM	04/08/98
<a href="#">CY7C1049BV33</a>	512K x 8 Static RAM	03/29/01
<a href="#">CY7C1049V33</a>	512K x 8 Static RAM	07/09/01
<a href="#">CY7C106</a>	256K x 4 Static RAM	07/12/98
<a href="#">CY7C106B</a>	256K x 4 Static RAM	03/23/01
<a href="#">CY7C109</a>	128K x 8 Static RAM	09/10/99
<a href="#">CY7C109B</a>	128K x 8 Static RAM	03/23/01
<a href="#">CY7C109V33</a>	128K x 8 Static RAM	09/07/99
<a href="#">CY7C128A</a>	2K x 8 Static RAM	02/08/00
<a href="#">CY7C1399</a>	32K x 8 3.3V Static RAM	03/26/99
<a href="#">CY7C1399B</a>	32K x 8 3.3V Static RAM	07/19/01
<a href="#">CY7C1399V</a>	32K x 8 3.0V Static RAM	06/05/96
<a href="#">CY7C148</a>	1K x 4 Static RAM	11/23/93
<a href="#">CY7C149</a>	1K x 4 Static RAM	11/23/93
<a href="#">CY7C150</a>	1K x 4 Static RAM	12/22/92
<a href="#">CY7C164</a>	16K x 4 Static RAM	02/08/00
<a href="#">CY7C166</a>	16K x 4 Static RAM	02/08/00
<a href="#">CY7C167A</a>	16K x 1 Static RAM	02/08/00
<a href="#">CY7C168A</a>	4K x 4 Static RAM	02/08/00
<a href="#">CY7C182</a>	8K x 9 Static RAM	10/05/99
<a href="#">CY7C185</a>	8K x 8 Static RAM	08/21/98
<a href="#">CY7C185A</a>	8K x 8 Static RAM	10/05/99
<a href="#">CY7C187</a>	64K x 1 Static RAM	10/05/99
<a href="#">CY7C188</a>	32K x 9 Static RAM	05/29/96
<a href="#">CY7C192</a>	64K x 4 Static RAM with Separate I/O	10/05/99
<a href="#">CY7C194</a>	64K x 4 Static RAM	09/29/99
<a href="#">CY7C195</a>	64K x 4 Static RAM	09/29/99
<a href="#">CY7C196</a>	64K x 4 Static RAM	09/29/99

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Part Number	Part Description	Last Updated
<a href="#">CY7C197</a>	256K x 1 Static RAM	10/05/99
<a href="#">CY7C199</a>	32K x 8 Static RAM	04/24/98
<b>MoBL™ &amp; Micropower - Low Power Asynchronous SRAMs</b>		
<a href="#">CY6264</a>	μPower 8K x 8 Static RAM	07/03/96
<a href="#">CY62256</a>	μPower 32K x 8 Static RAM	12/02/97
<a href="#">CY62256V</a>	μPower 32K x 8 Static RAM	05/20/98
<a href="#">CY62256V25</a>	μPower 32K x 8 Static RAM	05/20/98
<a href="#">CY62256V18</a>	μPower 32K x 8 Static RAM	05/20/98
<a href="#">CY62126BV</a>	MoBL™ 64K x 16 Static RAM	06/15/00
<a href="#">CY62126V</a>	μPower 64K x 16 Static RAM	02/13/98
<a href="#">CY62127BV</a>	MoBL 64K x 16 Static RAM	06/15/00
<a href="#">CY62127V</a>	μPower 64K x 16 Static RAM	06/08/99
<a href="#">CY62128</a>	μPower 128K x 8 Static RAM	06/19/98
<a href="#">CY62128B</a>	μPower 128K x 8 Static RAM	03/29/01
<a href="#">CY62128V</a>	μPower 128K x 8 Static RAM	03/21/01
<a href="#">CY62128V25</a>	μPower 128K x 8 Static RAM	03/21/01
<a href="#">CY62128V18</a>	μPower 128K x 8 Static RAM	03/21/01
<a href="#">CY62136V</a>	MoBL 128K x 16 MoBL Static RAM	02/02/01
<a href="#">CY62136BV18</a>	MoBL2™ 128K x 16 MoBL Static RAM	03/01/01
<a href="#">CY62137V</a>	MoBL 128K x 16 MoBL Static RAM	02/02/01
<a href="#">CY62137CV18</a>	MoBL2 128K x 16 MoBL Static RAM	04/26/01
<a href="#">CY62137BV18</a>	MoBL2 128K x 16 MoBL Static RAM	03/01/01
<a href="#">CY62138V</a>	MoBL 256K x 8 MoBL Static RAM	01/21/00
<a href="#">CY62143BV</a>	MoBL 256K x 16 Static RAM	06/12/01
<a href="#">CY62146V</a>	MoBL2 256K x 16 MoBL Static RAM	06/15/01
<a href="#">CY62146V18</a>	MoBL2 256K x 16 MoBL Static RAM	09/06/00
<a href="#">CY62146BV18</a>	MoBL2 256K x 16 MoBL Static RAM	05/02/01
<a href="#">CY62147V</a>	MoBL 256K x 16 MoBL Static RAM	06/15/01
<a href="#">CY62147V18</a>	MoBL 256K x 16 MoBL Static RAM	09/06/00
<a href="#">CY62147BV18</a>	MoBL 256K x 16 MoBL Static RAM	05/02/01
<a href="#">CY62147CV18</a>	MoBL 256K x 16 MoBL Static RAM	05/02/01
<a href="#">CY62147CV25/30/33</a>	MoBL 256K x 16 MoBL Static RAM	06/27/01

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Part Number	Part Description	Last Updated
<a href="#">CY62148</a>	μPower 512K x 8 Static RAM	03/14/01
<a href="#">CY62148B</a>	μPower 512K x 8 Static RAM	06/19/01
<a href="#">CY62148V MoBL</a>	MoBL 512K x 8 MoBL Static RAM	03/27/00
<a href="#">CY62148CV25/30/33</a>	MoBL 512K x 8 MoBL Static RAM	06/19/01
<a href="#">CY62157CV18</a>	MoBL 512K x 8 MoBL Static RAM	04/02/01
<a href="#">CY62157CV25/30/33</a>	MoBL 512K x 8 MoBL Static RAM	06/26/01
<a href="#">CY62158CV25/30/33</a>	MoBL 1024K x 8 MoBL Static RAM	06/27/01
2001 Application Note	<a href="#">Interfacing the QDR™ to the Delta39K™</a>	07/10/01
2001 Application Note	<a href="#">Introduction to Cypress SRAMs</a>	04/20/01
2001 Application Note	<a href="#">Interfacing the QDR™ with Altera APEX20KE</a>	03/12/01
2001 Application Note	<a href="#">NoBL™ SRAMs and Bus Contention</a>	03/12/01
2001 Application Note	<a href="#">Thermal Characteristics of SRAMs</a>	03/12/01
2000 Application Note	<a href="#">Interfacing the QDR™ to the XILINX SPARTAN-II FPGA</a>	02/21/00
2000 Application Note	<a href="#">Quad Data Rate™ (QDR) SRAM Clocking Scheme</a>	02/18/00
1999 Application Note	<a href="#">Pinout Compatibility Considerations of SRAMs</a>	06/30/99
1999 Application Note	<a href="#">Understanding Burst Modes in Synchronous SRAMs</a>	06/30/99
1999 Application Note	<a href="#">MoBL™: The New Mobile SRAM</a>	06/06/99
1998 Application Note	<a href="#">NoBL: The Fast SRAM Architecture</a>	06/29/98
1998 Application Note	<a href="#">NoBL, The ZBT-Compatible Family</a>	06/26/98