

Cypress Semiconductor CD-ROM

Development Tools

Part Number	Part Description	Last Updated
Third-Party Tool Support	Seamless Integration Path Between Cypress Warp Software and Third Party Vendor Software	03/26/97
CY3120/CY3120J	Warp [®] CPLD Development Software for PC	06/29/01
CY3125	Warp CPLD Development Tool for UNIX	06/29/01
CY3120AVHDL	Advanced VHDL and CPLD Design Class Kit	10/07/98
CY3128	Warp Professional [™] CPLD Software	06/29/01
CY3130	Warp Enterprise [™] VHDL CPLD Software	06/29/01
CY3138	Warp Enterprise Verilog CPLD Software	07/05/01
CY3620/3620J	Warp ISR [™] ISR Design Kit for CPLDs	03/20/01
CY3700i	Ultra37000 [™] ISR [™] Programming Kit	08/02/99
CY3600i	FLASH370i [™] : ISR Programming Kit	
CY3620/3620J	Warp ISR Design Kit for CPLDs	
	Cypress Federation[™]	2000
	PLD Development Tools Overview	03/04/97
2001 Application Note	Understanding the Warp[®] Report File for Ultra37000 Devices	02/26/01
2000 Application Note	Method to Instantiate and Use a Core in LeonardoSpectrum[™]	08/22/00
2000 Application Note	Method to Instantiate and Use a Core in Synplify[™]	08/23/00
2000 Application Note	Method to Instantiate and Use a Core in Warp Enterprise/Professional[™]	08/23/00
2000 Application Note	Method to Instantiate and Use a Core in Warp with Cypress CPLDs	08/21/00
2000 Application Note	Importing a Warp Post-fit Netlist Into Mentor Graphics' ModelSim[™]	08/22/00
2000 Application Note	Targeting Cypress ISR CPLDs with Synplify 6.0	08/08/00
1999 Application Note	An Introduction to Active-HDL[™] Sim	04/02/99
1999 Application Note	An Introduction to Active-HDL[™] FSM	04/02/99
1999 Application Note	Targeting Cypress PLDs from the Leonardo Spectrum Environment	08/10/00
1999 Application Note	Targeting Cypress PLDs from the Synopsys FPGA Express Environment	02/19/99
1998 Application Note	Design Optimization Using Warp Synthesis Directives	01/28/98
1998 Application Note	Targeting Cypress PLDs from the Cadence Environment	05/29/98
1994 Application Note	Using Hierarchy in VHDL Design	03/05/97