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SUMMER/FALL 2001

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SEGMENT SOLUTIONS

[Cypress Unveils Market Segment Organization to Further Accelerate Growth](#)

Cypress enhances its focus on fast-growing communications markets by deploying four new market segment business units – Wide Area Network, Storage Network, Wireless Terminal, and Wireless Infrastructure – that cut across its traditional product divisions.

[Click here to read the press release](#)

Website Survey

What's your pet peeve in supplier websites? How can we serve you better? Participate in our [website survey](#) and enter for a chance to win one of 10 Handspring Visors.™

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[New Cypress Screensaver!](#)

NEWS & EVENTS

[Cypress showcases its Peak Reducing EMI Solutions \(PREMISTM\) at the IEEE EMC Symposium](#)

Cypress will highlight its Spread Spectrum Clock Generator (SSCG) ICs within its PREMISTM product family at the IEEE EMC Symposium in the Montreal Convention Center from August 13 through August 17, 2001.

[EZ-USB® FX2™ Receives USB 2.0 Certification](#)

The Cypress EZ-USB® FX2™ is the first hi-speed controller to pass the full suite of USB 2.0 compliance tests set by the USB Implementers Forum. The EZ-USB FX2 family makes it possible for a wide array of applications to take advantage of USB 2.0's high speed and ease of connectivity.

[Cypress Completes Lara Acquisition](#)

Cypress completed its acquisition of Lara Networks and its solutions for OC-768 and 10 Gigabit Ethernet. Lara's network search engines (NSEs) and network coprocessors are widely used by leading networking OEMs.

[Cypress-Lucent Strategic Supplier Pact](#)

Cypress and Lucent have agreed to share critical technology information early in the design cycle. The agreement is the first of its kind between a major communications systems company and a semiconductor supplier.

NEW PRODUCTS

[Cypress Now Shipping 8 Mb NetRAMs™](#)

Cypress introduces the industry's first 8 Mb NetRAM™ memories for SAN servers and network switches, along with 1 Mb and 4 Mb versions. The family are pin-compatible density upgrades for the Motorola NetRAM synchronous SRAMs.

[Cypress Samples 200,000-Gate CPLDs](#)

The Delta39K200 offers 200,000 gates with 3,072 macrocells and 480 Kbits of embedded communications memory. The world's largest CPLD, it is targeted at high-performance communications applications.

[QuadPort™ DSE Family for WAN, SAN and WIN Applications](#)

Cypress expands its QuadPort™ Datapath Switch Element (DSE) family. The non-blocking switch architecture created by their combination of logic and memory enables the QuadPort DSEs to be used as communications data path managers.

[USB 2.0 Mass Storage Reference Design Kit](#)

The CY4611 developers kit uses Cypress's EZ-USB® FX2™ to provide a high-performance USB 2.0-to-ATA bridge solution supporting IDE and ATAPI data transfer modes and CD-RW, DVD, magneto-optical and hard disk media.

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