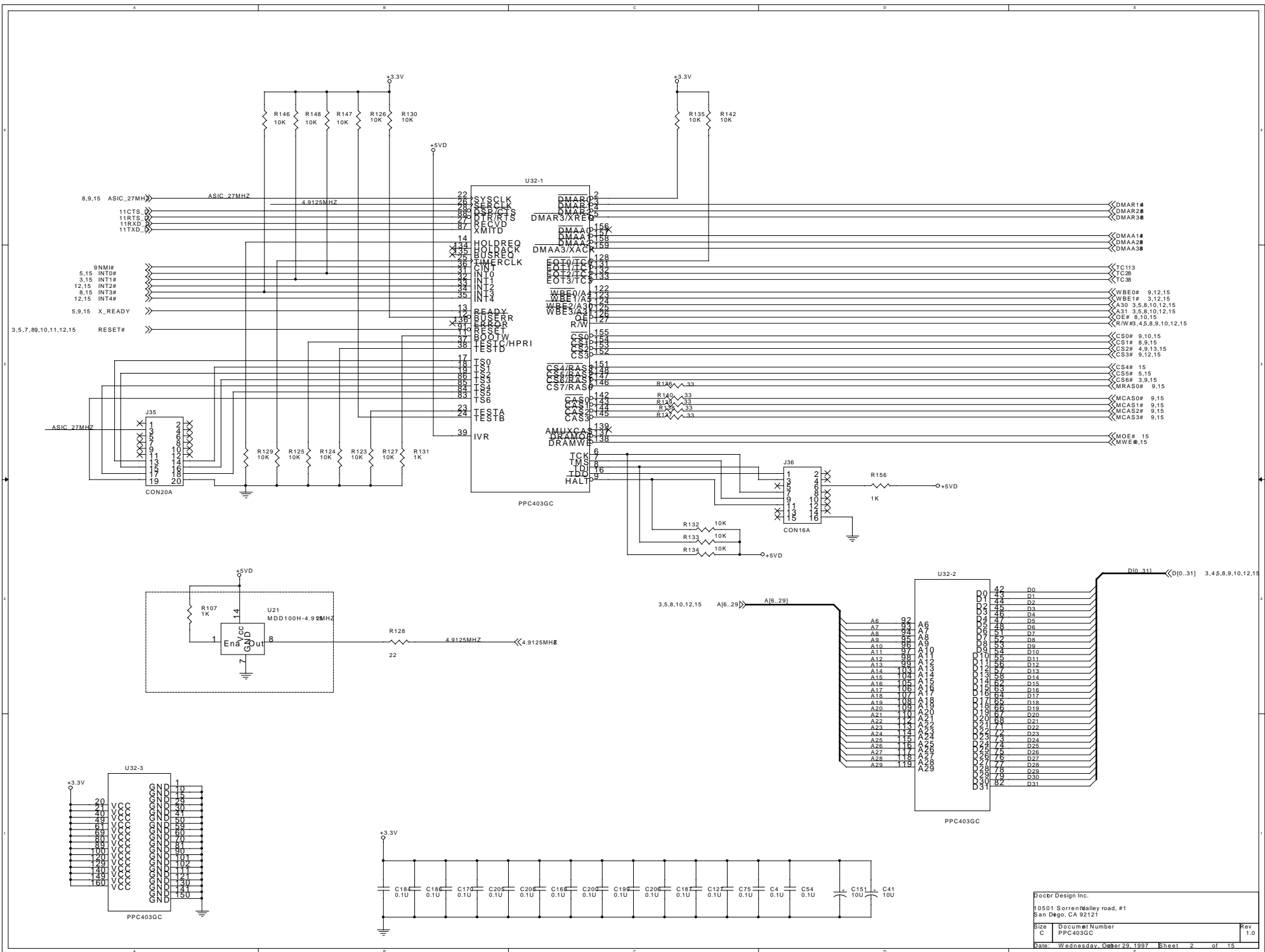
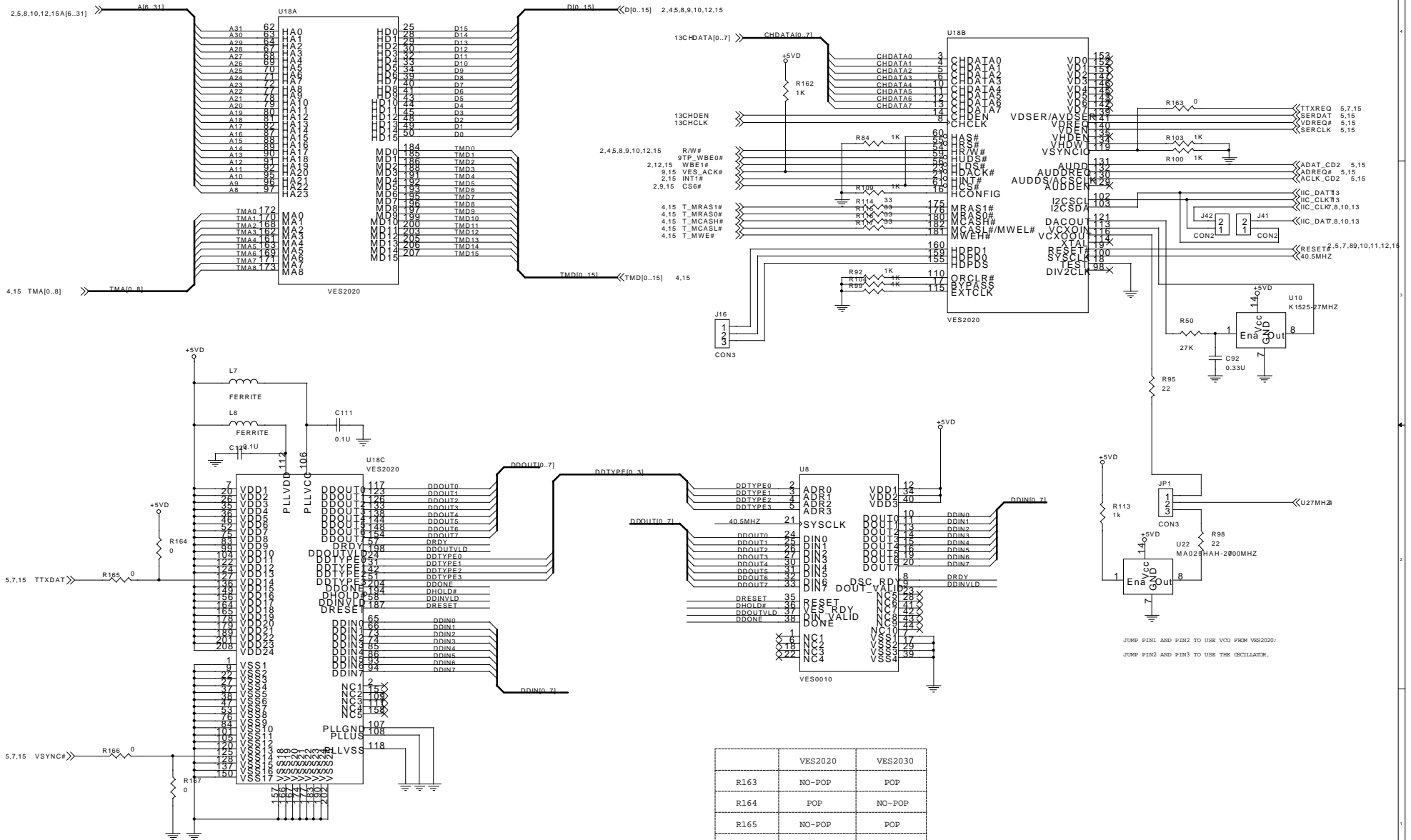


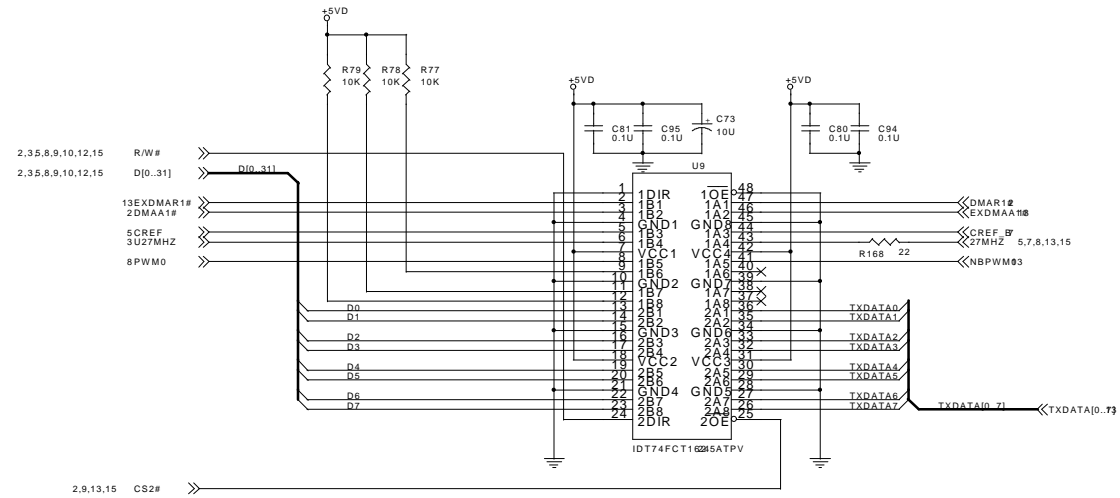
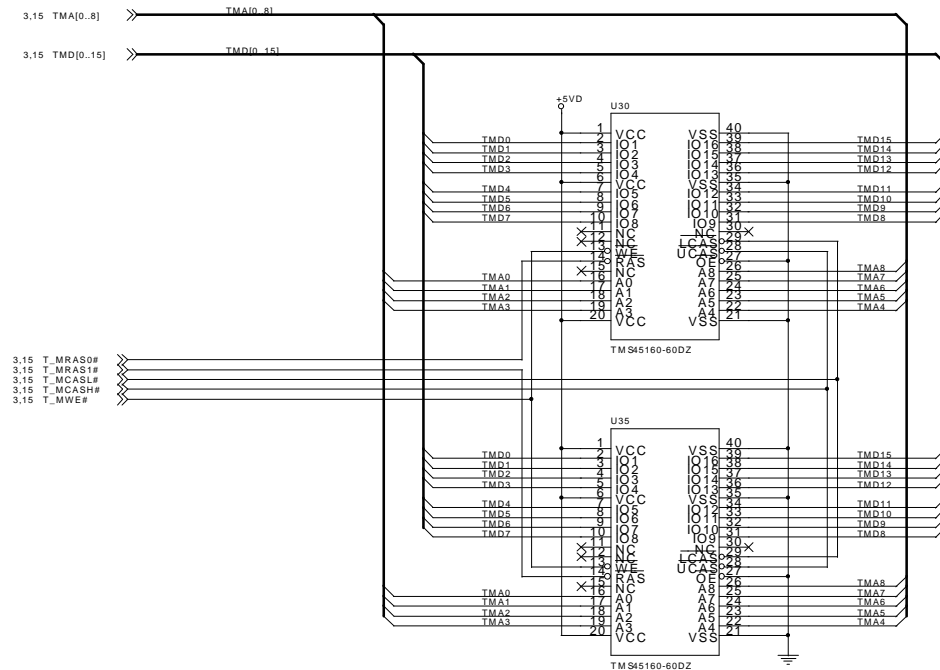
- P1. COVER PAGE
- P2. PPC403GC
- P3. TRANSPORT CHIP
- P4. TRANSPORT DRAM AND BUFFER
- P5. IBMCD21 MPEG2 DECODER
- P6. CD21_DRAM
- P7. VIDEO/AUDIO
- P8. STB PERIPHERAL ASIC
- P9. GLUELOGIC AND DRAM_SIMM
- P10. POWER AND FLASH MEMORY
- P11. IrDA, DEBUG PORT AND RESET
- P12. EXPANSION BUFFER
- P13. EXPANSION AND FRONT END CONNECTORS
- P14. CAPACITORS
- P15. TEST POINTS

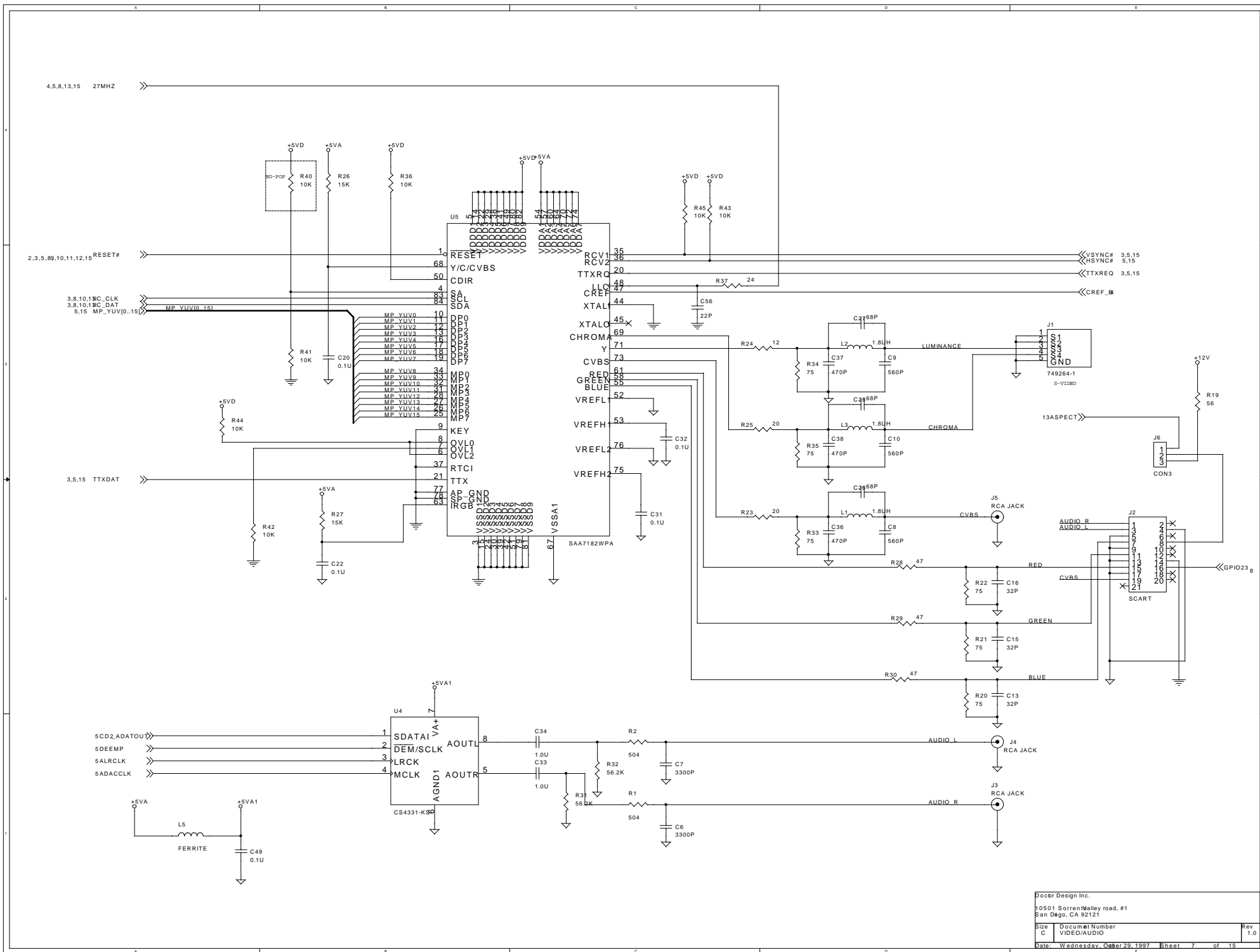
STB Reference Design Kit Schematics

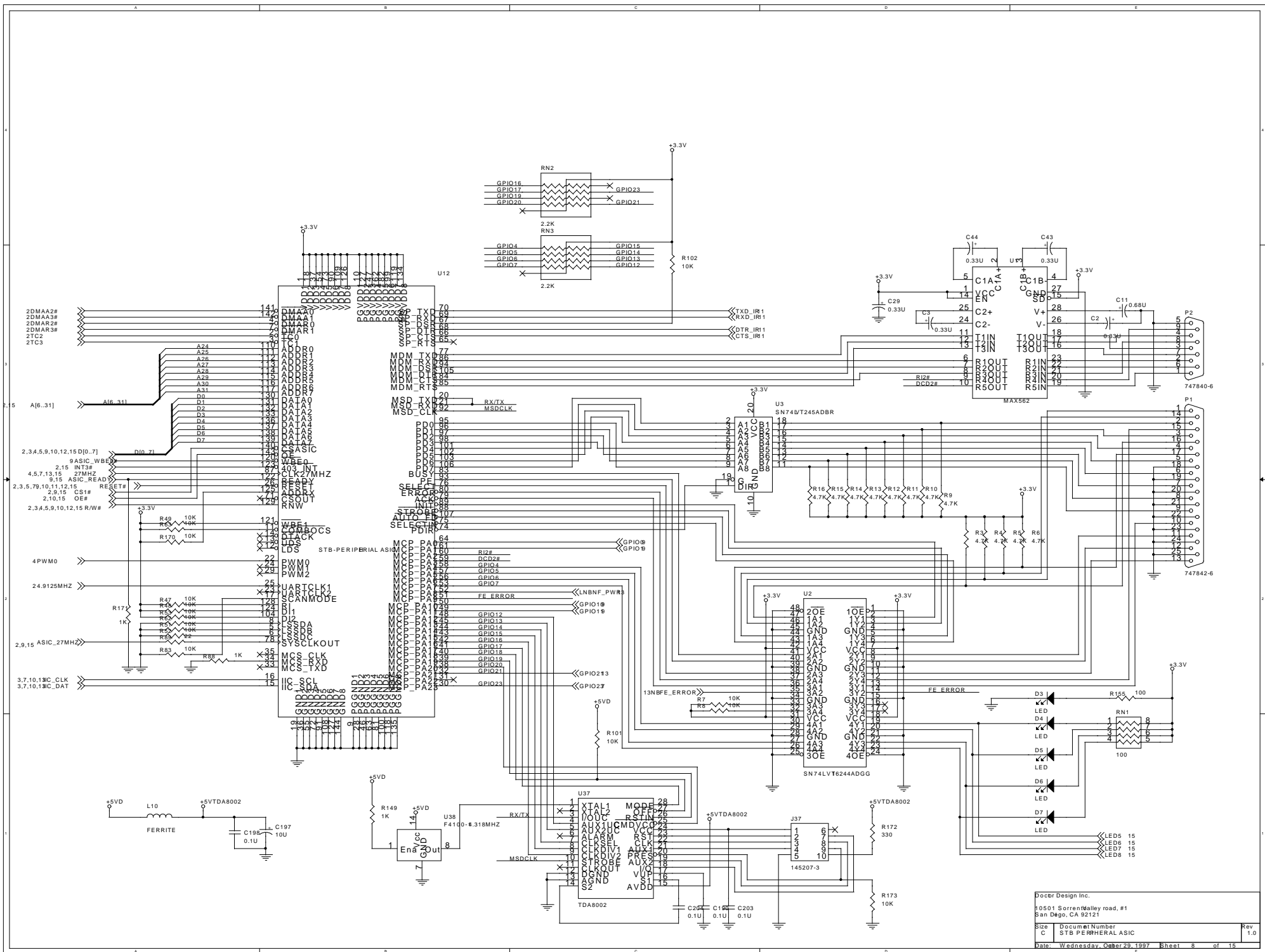


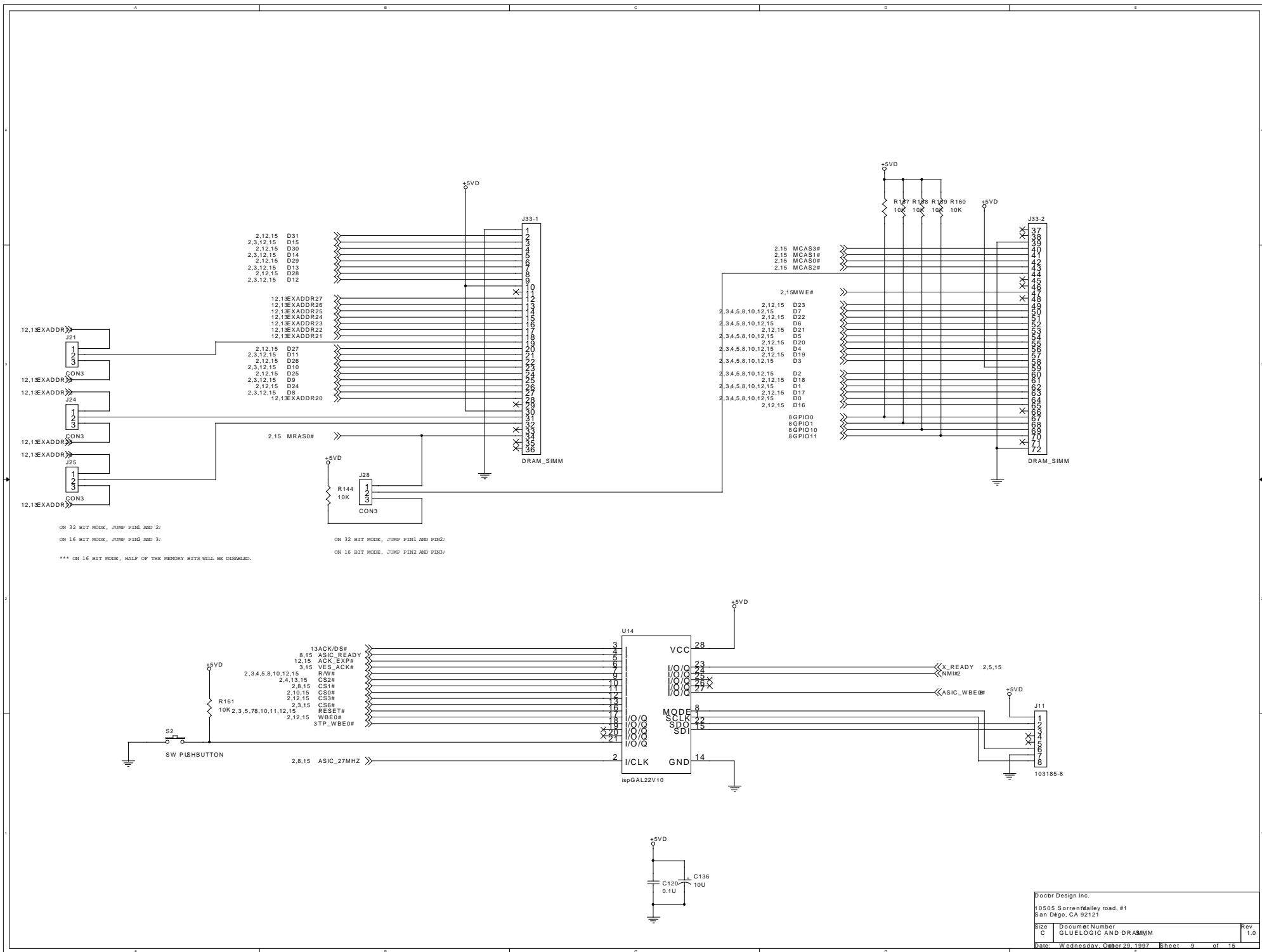


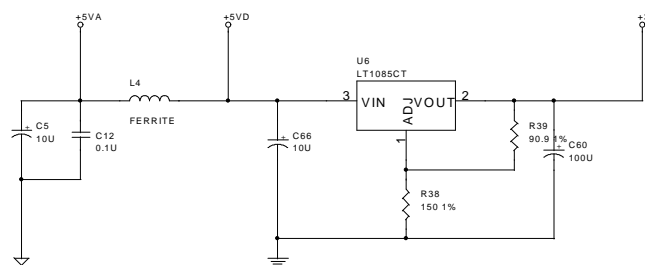
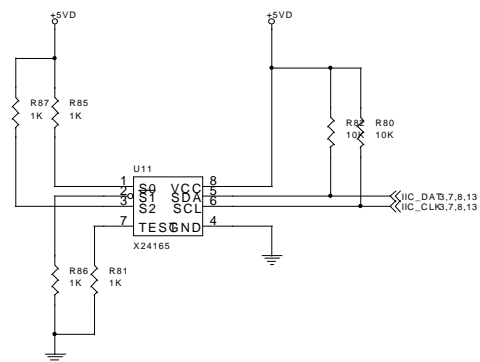
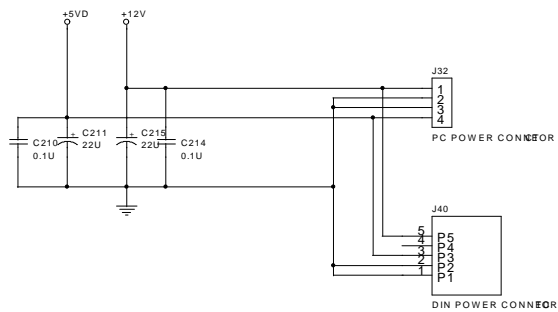
	VES2020	VES2030
R163	NO-POP	POP
R164	POP	NO-POP
R165	NO-POP	POP
R166	NO-POP	POP
R167	POP	NO-POP
R100	POP	NO-POP











2,3,5,8,12,15 A[6..31]

2,9,15 CS0#
2,3,5,7,8,9,11,12,15 RESET#
2,8,15 OE#
2,3,4,5,8,9,12,15 R/W#

