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0.18-Micron ASIC Gate Array and Standard Cell Families
DALLAS (Sept. 16, 1996) -- Texas Instruments (TI) today unveiled its 0.18-micron ASIC (Application Specific Integrated Circuits) product offering which will alter the way system architectures are designed, enabling single-chip systems originally envisioned for the 21st Century. Utilizing TI's 125-million transistor TIMeline Technology, the TGC6000 Series CMOS gate array, TEC6000 Series CMOS embedded array and TSC6000 Series CMOS standard cell families will deliver a powerful combination of system integration and ultra-low power capabilities. These families will provide designers with virtually unlimited flexibility to design a new generation of innovative and energy-efficient computer, consumer and communications systems.