



You are encouraged to join Xilinx at the local presentation of "[The Power of Innovation](#)." You will benefit from the interaction of a live presentation, demonstrations, and question & answer discussions. If you cannot attend a live seminar, contact your local [sales office](#) for a presentation to your company. The following presentation pages are provided for on-line reference after the seminar, or for customers who otherwise cannot attend a local presentation.

Section 1	<a href="#">The Future of Programmable Logic</a>
Section 2	<a href="#">Product Overview</a>
Section 3	<a href="#">Case Study #1 - DRAM Controller</a>
Section 4	<a href="#">Case Study #2 - USB Interface</a>
Section 5	<a href="#">Case Study #3 - PCI Interface</a>
Section 6	<a href="#">Case Study #4 - DSP</a>
Section 7	<a href="#">The Next Generation</a>
Section 8	<a href="#">Summary</a>
Appendix A	<a href="#">Reference Information</a>
Appendix B	Software Demo Screen Images <a href="#">CPLD Demo Using Foundation VHDL Tools</a> <a href="#">Constraint Entry Using Synopsys FPGA Express</a> <a href="#">Core Configuration Tool for PCI</a> <a href="#">M1 Software Tools</a>