



# 1997 Spring Seminar Series

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*Held May/June 1997 at a Location Near You!*

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## Overview



Over the past decade, programmable logic has evolved as a primary building block in complex system design and development. No longer viewed as a simple logic alternative, today's programmable logic devices are dramatically altering the way we design. From simple address decoders to lightning fast digital signal processors, PLDs offer the performance, density and software utility necessary to fuel tomorrow's technologies. Join Xilinx as we examine the role of programmable logic and "The Power of Innovation".

## Agenda:

**8:30 a.m. REGISTRATION AND CONTINENTAL BREAKFAST**

**9:00 a.m. INTRODUCTION**

### **The Future of Programmable Logic**

- Why programmable logic?
- The latest low-power devices
- Trends in density and performance
- New design methodologies

## **9:45 a.m. PRODUCT OVERVIEW**

### **Part 1: Software and Intellectual Property**

- Open systems: The power of EDA vendor integration
- Ready-to-use software: VHDL, synthesis, and more
- Pre-verified cores: Design reuse means fast time to market
- New powerful tools

### **Part 2: FPGAs and CPLDs**

- Big and fast: Low-power FPGAs that operate at 5V and 3.3V
- XC4000XL: The highest density, low-power, low-voltage FPGA
- XC4000E: The fastest 5V FPGA
- XC9500: Low-cost CPLDs with guaranteed pin locking
- XC5200: A new low-cost breed of FPGAs to replace gate arrays

## **11:45 a.m. LUNCH**

## **1:00 p.m. CASE STUDIES**

Real world scenarios to evaluate, verify and optimize programmable logic designs.

- Learn how to implement and verify a low cost CPLD design requiring pin locking using the new Xilinx VHDL-based, push-button Foundation software tools.
- Use a Verilog-based constraint driven core tool that provides push-button synthesis design flows. Let the design grow without worry thanks to footprint compatibility with larger XC4000XL FPGA devices.
- See how simple it is to implement PCI designs in the XC4000E and XC4000X FPGA devices and cut months off design cycles. Using drop-in, pre-tested LogiCORE software modules, you can easily meet the stringent timing constraints of the PCI specification.
- Learn why more and more designers are choosing FPGAs for high-performance digital signal processing applications. Implement DSP functions quickly and simply from a GUI-based software CORE generator in new low-power 3.3V devices.

## **3:30 p.m. THE NEXT GENERATION**

Imminent breakthroughs in programmable logic.

- Programmable logic is now driving the advanced CMOS semiconductor manufacturing processes. As a result, performance and density are going up, and cost is coming down at an unprecedented rate. Find out about the next generation of FPGAs from the industry leader and how these exciting products will change the way you use programmable logic. These devices will offer up to 400,000 logic gates at system speeds of 100MHz and above and include features such as hierarchical memory support.

## FAQs:

**Q.** Will there be a seminar in my area?

**A.** See our [Seminar Locations](#) page. If you don't find a seminar near you, contact your local [Xilinx sales office](#).

**Q.** How do I get to the seminar hotel?

**A.** Addresses for all the hotels are included on the [Seminar Locations](#) page. You may also be able to [get an online map via Yahoo!](#).



**Q.** Is Xilinx offering a special room rate at the seminar hotel?

**A.** No, please contact the hotel directly to book overnight accommodations.

**Q.** Is there a charge for the seminar?

**A.** No, the seminars are free of charge.

**Q.** Are meals being served during the day?

**A.** A continental breakfast and lunch will be served.

**Q.** Do I need to bring anything?

**A.** No, everything you need for the seminar will be provided.

**Q.** What is the dress code?

**A.** Casual.

**Q.** How do I register?

**A.** Starting April 21st, the top of this page will contain a link to allow you to register on-line.

**Q.** Whom do I contact if I need more information?

**A.** For More Information: call 1-888-261-0971