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FOR IMMEDIATE RELEASE

**XILINX/SYNOPSYS COLLABORATION RESULTS  
IN TIGHTLY INTEGRATED PC SYNTHESIS SOLUTION**

*Synopsys FPGA Express Allows Xilinx Customers to Synthesize Designs from VHDL  
or Verilog Sources Without Sacrificing Chip Resources or Performance*

LAS VEGAS, Nev., Design Automation Conference, June 3, 1996—Xilinx, Inc., (NASDAQ:XLNX) today announced that its technology partnership with Synopsys has resulted in a tightly integrated PC synthesis solution that provides users with high quality synthesis results for silicon area utilization, place-and-route predictability and performance with Xilinx devices.

“Synthesis that takes full advantage of the innovative architectures offered by Xilinx is critical to the success of our customers,” said Chuck Fox, vice president of marketing at Xilinx. “By working closely with Synopsys to integrate architectural knowledge into FPGA Express, we’ve enabled our customers to employ powerful, yet easy-to-use, PC-based synthesis specifically optimized for our FPGA and CPLD devices.”

FPGA Express has built in intelligence for features in supported Xilinx devices such as smart use of carry logic and optimized look up table mapping algorithms for highly efficient area utilization and performance.

FPGA Express also automatically outputs XACTstep 6.0 netlists and constraint files to reduce run time and allow easy integration with XACTstep tools. In addition, the software contains everything necessary to integrate Xilinx LogiCore system level

— more —

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modules, such as PCI bus interface, into the design flow for access to customized, “drop-in” logic blocks to further cut development time.

The easy-to-use FPGA Express interface, combined with the integration of Xilinx architecture features and XACTstep 6.0 implementation software, will enable a user to get excellent synthesis results without being a device or design tool expert.

### **Pricing and Availability**

Sold by Synopsys, FPGA Express starts at \$12,000 and will be available in full production in September, running on Windows '95 and Windows NT-based personal computers. Xilinx architectures supported will include the following families of FPGA and CPLD devices: XC3000A, XC3100A, XC4000E, XC4000EX, XC5200, XC7300, and XC9500.

Founded in 1984, Xilinx is the world's largest supplier of programmable logic solutions comprising industry leading device architectures and world class design software. Headquartered in San Jose, Calif., the company pioneered the market for field programmable gate array (FPGA) semiconductor devices that provide high integration and quick time-to-market for electronic equipment manufacturers in the computer, peripheral, telecommunications, networking, industrial control, instrumentation, and high reliability/military markets.

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To the extent that matters covered in this release are forward looking statements, they involve risks and uncertainties, including timely availability and acceptance of new products, business conditions, the growth of the programmable logic market, and other risks that are described in the company's SEC filings, including the Form 10-Q for the quarter ended March 30, 1996.

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