

**ANSWERS****Technical Support**

File Download: app_dsp

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FPGA Applications involving Digital Signal Processing

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| Filename | Size | File Description |
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| fft.zip | 12 kB | This is an example VHDL design of a Fast Fourier Transform for a Digital Processing System. Please refer to the enclosed readme.txt file for further details. |
| xamfir.zip | 832 kB | Word .DOC files for FIR filter applications |
| dspdev.ppt | 1083 kB | Xilinx Applications DSP Presentation |
| fir_filt.zip | 308 kB | Viewlogic files for "16-tap, 8-bit FIR Filter" application note For All Platforms Uploaded: 12-02-96 |
| xdsptar.z | 7653 kB | DSP Toolbox library. Created for use with Mentor A.4 or B.x For All Unix Uploaded: 03-10-97 |