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3rd party interfaces documentation that are supported by Xilinx.

Filename	Size	File Description
config.pdf	1,112KB	Foundation Configuration Information
manhde.pdf	798KB	Foundation HDL Editor Users guide
manpcm.pdf	734KB	Foundation Design Process Management (How to use Project Manager for Foundation)
mansc.pdf	5,530KB	Foundation Schematic Editor Users Guide
mansim.pdf	3,655KB	Foundation Logic Simulator Users Guide
Mentor.pdf	2,671KB	Mentor Graphics Interface/Tutorial Guide
notes.pdf	413KB	Misc. notes on using the Foundation tools
orcad.pdf	1,832KB	OrCAD Interface/Tutorial Guide
promfile.pdf	649KB	PROM File Formatter Reference/User Guide
timesim.pdf	154KB	Foundation Timing Simulation Tutorial
vhdl_mug.pdf	838KB	Metamor version 2.3 Users

		Guide (VHDL Compiler for Foundation toolset)
<u>vlogint.pdf</u>	107KB	Full documentation for ES-VERILOG v5.2. Interface to Verilog-XL
<u>vlintrfc.pdf</u>	2,121KB	ViewLogic Interface Guide
<u>vltutor.pdf</u>	2,557KB	Viewlogic Tutorials (Pro-Series based)
<u>xabel.pdf</u>	1,459KB	Xilinx ABEL User Guide
<u>xlxman.pdf</u>	444KB	Foundation-Xilinx Interface Guide
<u>xsi_fpga.pdf</u>	1,621KB	Synopsys (XSI) For FPGAs Interface/Tutorials Guide
<u>xsi_note.pdf</u>	149KB	XACTstep Version 5.2/6.0 Synopsys Release Notes