

New XC4000 Family Speed Grade Offers Higher Performance

The XC4000 FPGA family will reach new performance levels with the introduction of the -3 speed grade this July. This speed improvement, along with upcoming architectural improvements, will expand the range of applications that can be addressed by this high-performance, full-featured FPGA family. The improved performance of the -3 devices also allows the XC4000 family to be fully PCI compliant.

In the typical application, the new -3 speed grade offers a 25 percent performance improvement over the previous XC4000 device speed record. System clock speeds of 70

MHz and higher will be achievable.

Digital signal processing is among the many high-performance applications that can be addressed by this high-speed FPGA technology. Video processors that previously required multiple DSP processors or large ASICs can be implemented in a single FPGA device. (An application note about FIR filter implementations in XC4000 FPGAs was recently released, and more DSP design support material is being prepared.). For more information, send E-mail inquiries to dsp@Xilinx.com



MIL-STD-883B Compliant Serial PROMs

The XC1765D and XC17256D Serial Configuration PROMs are now available in full MIL-STD-883B compliant versions. These devices typically are used to store configuration data for Xilinx SRAM-based FPGAs, and are optimized for easy use with the FPGAs. As part of our commitment to the Hi-Rel (military, defense, and aerospace) market, Xilinx has long offered military temperature range versions, but in addition now provides fully compliant devices.

The XC1765D device holds 65,536 bits of data and can completely config-

ure Xilinx FPGAs up to the density of the XC3090, while the XC17256D holds 262,144 bits of data and can completely configure any Xilinx FPGA up to the density of the XC4013. Multiple devices can be easily cascaded to support multiple FPGAs and/or multiple configurations.

Both devices are available in the 8-pin DIP package, and DESC SMDs (Standard Military Drawings) have been released. These devices are supported by a wide range of programmers available from Xilinx and leading third-party vendors. ♦