

XC5200 FPGA Family Grows

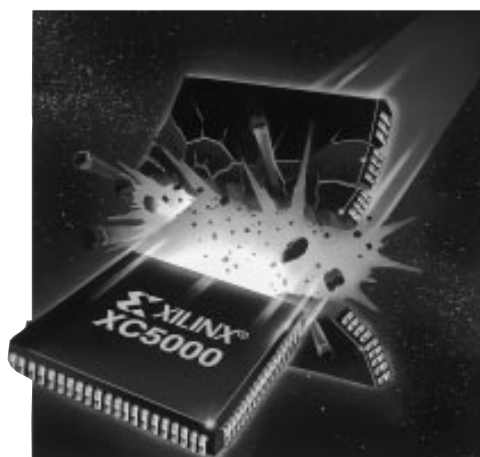
The XC5200 FPGA family has been expanded to include the 2,500-gate XC5202 and the 4,000-gate XC5204. The XC5200 family now includes flexible, high-density FPGA devices ranging from 2,500 to 18,000 gates.

For lower-density designs that do not

require the use of on-chip user RAM, the new XC5202 and XC5204 FPGAs provide a feature-rich solution. Like all members of the XC5200 FPGA family, these new devices include JTAG boundary-scan logic for enhanced testability, carry logic supporting fast arithmetic operations, and internal 3-state buffers for efficient on-chip busing capability.

All XC5200 devices offered in a common package (*see table*) are completely footprint compatible, allowing the easy migration to smaller or larger devices without changes to the PCB layout. The flexible VersaRing™ I/O interface delivers the industry's best pin locking capability for a high-density FPGA.

Please contact your local Xilinx sales representative for further information regarding software and component availability. ♦



DEVICE	NEW XC5202	NEW XC5204	XC5206	XC5210	XC5215
Usable gates	2,200-2,700	3,900-4,800	6,000-7,500	10,000-12,000	14,000-18,000
VersaBlock matrix	8 x 8	10 x 12	14 x 14	18 x 18	22 x 22
Total CLBs	64	120	196	324	484
Total Flip-Flops	256	480	784	1,296	1,936
Total IOBs	84	124	148	196	244
10K Unit Pricing (-6 speed grade, PC84)	\$9	\$15	\$25	\$38	\$68 (PQ208)
Availability	3Q95	3Q95	NOW	NOW	3Q95
Packages	PC84 PQ100 VQ100 TQ144 PG156	PC84 PQ100 VQ100 TQ144 PQ160 PG156	PC84 PQ100 TQ144 PQ160 PQ208 PG191	PC84 TQ144 PQ160 PQ208 PQ240 PG223	 PQ208 PQ240 HQ304 PG299