

Acquiring Application Notes and Design Files

Xilinx constantly strives to provide application notes on topics important for programmable logic users. Table 1 lists some of the newer application notes, including the targeted device family and available design files:

Table 1. *Recently Published Xilinx Application Notes.*

TITLE	FAMILY	DESIGN FILES	XDOCS XFACTS	WEB
XC4000 Edge-Triggered and Dual-Port RAM Capability	XC4000E		80004	Yes
Implementing FIFOs in XC4000E RAM	XC4000E	VIEWdraw	80003	Yes
16-Tap, 8-Bit FIR Filter Applications Guide	XC4000/XC4000E	VIEWdraw	80000	Yes
Plug and Play ISA in an XC4000	XC4000/XC5200	VIEWdraw + ABEL	80007	Yes
2910-Compatible Microprogram Sequencer in an XC4000 FPGA	XC4000	VIEWdraw		Yes
Dynamic Microcontroller in an XC4000 FPGA	XC4000	VIEWdraw	80005	Yes
Designing Flexible PCI Interfaces With Xilinx EPLDs	XC7300	ABEL, VHDL		Yes
EPLD-Based Segmented Elastic Frame Store for ATM	XC7300		80010	Yes
Fully Compliant PCI Interface in an XC3164A-2 FPGA	XC3100A	Verilog, VIEWdraw	80006	Yes
Configuring FPGAs Over a Processor Bus	All SRAM FPGAs	VIEWdraw	80011	Yes
Pulse-Width Modulation in Xilinx Programmable Logic	All	VIEWdraw	80009	Yes
Voltage Monitoring for XC2000 Devices	XC2000		80008	Yes

Family — The family for which the application note was written.

Design Files — If indicated, this column shows which design files and formats are available.

XDOCS/XFACTS — If indicated, use this number to retrieve the document from the XDOCS E-mail server or from the XFACTS fax server.

- For XDOCS, send an E-mail message to 'xdocs@xilinx.com' with 'send <number>' in the subject header. The server will automatically send a uuencoded and compressed PostScript file that you can send to any PostScript-compatible printer. (You can also send an E-mail to XDOCS with 'help' in the subject header to retrieve full instructions on using XDOCS.)
- For XFACTS, just dial 1-408-879-4400 and specify the document number along with your return fax number.

Web

If "yes," the document is available on the World Wide Web. The Xilinx home page, called webLINX, can be found at '<http://www.xilinx.com/>'. The document can be found under Application Notes, which is under Product Information. The documents are in Adobe Acrobat™ format. If you do not already have the Acrobat reader installed on your machine, then you can jump to the Adobe home page to download it. ♦

IMPORTANT:

Updated Package Pinout Information

If you are designing with the following devices and packages, please be aware that pinout information in the *1994 Xilinx Programmable Logic Data Book*, third edition, is incorrect for **XC4010BG225**, **XC4013BG225** and **XC4025**.

Updated pinout information is available on the Xilinx WebLINX under Part Information, on the Xilinx Bulletin Board (filename "newpins.zip") and via XFACT/XDOCS (request document #80012). ♦