

XC4000E in Volume Production

New -2 Speed Grades Sampling! Software Shipping Today!

The XC4000E family has entered full-volume production. Xilinx is now able to satisfy the rapidly growing demand for the industry's highest-performance, full-featured FPGA.

Production volumes of the -5, -4, and -3 speed grades are available, and sampling has begun for the fastest speed grade, the -2. The new -2 speed grade propels the XC4000E to a higher than 60 MHz typical system speed and, like the -3 speed grade, is fully PCI compliant.

The XC4000E software began shipping in January. This XACT-Development™

software update provides access to all the advanced features of the XC4000E architecture, including the Select-RAM™ memory and the I/O clock enables. It will be shipped free of charge to current in-warranty customers upon request. Contact your local Xilinx sale representative or call Customer Service at 408-559-7778.

XC4000E users can take full advantage of this advanced FPGA architecture to accelerate their time-to-market through the use of the new Xilinx LogiCore™ modules. The inaugural LogiCore module is a fully verified PCI interface based on the XC4000E architecture. (See page 27.) ♦

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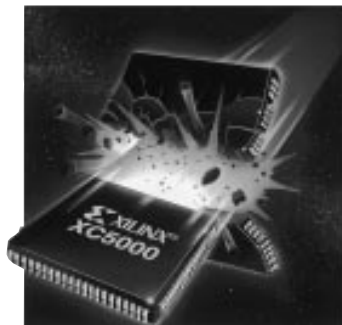
**DSP
Application
Design
Contest!**

Would you like to see your name in print? Would you like to win a Xilinx XC4000E t-shirt and either a CD-ROM drive with the latest Xilinx Development Software Package, a multimedia board (and speakers) for your PC, or one of several other prizes? Xilinx is looking for the five most innovative XC4000E-based DSP designs using XC4000E/XC4000EX Select-RAM™ memory. Xilinx will judge all entries received by May 30. The winning design ideas will be featured in the 3Q96 edition of *XCELL*. So what's the catch? You must be willing to allow Xilinx to print and share your design

idea with other designers, and promise to wear your Xilinx t-shirt with pride.

If you need some ideas to get started, check out our DSP application notes on our World Wide Web site (<http://www.xilinx.com>).

Send your schematic drawings, a diskette with your XC4000E/EX design files, and any other relevant documentation to: **Xilinx DSP Designer Challenge**, Xilinx Product Marketing, 2100 Logic Drive, San Jose, CA 95124 USA. For more information, email inquiries to dsp@xilinx.com or fax them to 408-879-4442, ATTN: DSP Design Challenge. ♦



XC5200 Family on Record Pace

XC5200 FPGA family revenue for the third fiscal quarter (ended December 30, 1995) increased by more than 150% from the previous quarter — the fastest ramp up of any new product in Xilinx history. The XC5200 family already is being used in hundreds of high-volume applications, including communications, data processing, networking and industrial systems.

The XC5200 family's unprecedented success results from a cost-optimized

design that takes full advantage of a 0.6μ three-layer-metal SRAM process technology and architectural innovations such as the VersaRing™ I/O interface. The XC5200 family includes five members, ranging from 2,000 to 18,000 usable gates. The devices are offered in 15 different packages, with more than 100 different device/package/speed combinations. ♦