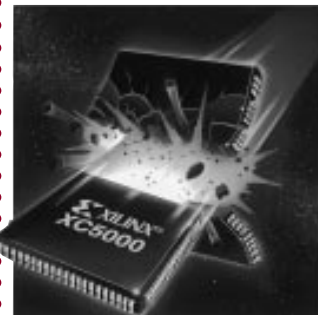


XC5200 Family:

Lower Prices & Higher Performance

Xilinx recently announced significant price reductions and performance improvements for the XC5200 FPGA family, further

advancing its position as the industry's most cost-effective FPGA family.



Prices Reduced by up to 50%

XC5200 sales have risen at a record pace. The rapid increase in shipments and a shift to a 0.5 μ process have resulted in significant cost advantages; these savings are being passed on directly to our users. With its segmented interconnect structure and an architecture optimized for its sub-micron, triple-layer metal (TLM) process, the XC5200 FPGA family has a significant technological advantage over other architectures. The family's small die sizes — in some cases even approaching today's gate array die sizes — lead to correspondingly low component costs.

The chart below shows current high-volume pricing and projected pricing one year from now.

XC5200 Family Volume Pricing*

DEVICE	MAXIMUM LOGIC GATES	END OF 1996	2H97
XC5202	3K	\$5.00	\$4.50
XC5204	6K	\$10.00	\$8.00
XC5206	10K	\$17.00	\$12.00
XC5210	16K	\$27.00	\$18.00
XC5215	23K	\$47.00	\$33.00

* Plastic package, -6 speed grade, 25,000 unit quantity, U.S. direct pricing only

Performance Improved by 30%

In addition to price reductions, a patented charge-pump transistor design has been added to the XC5200 family. When combined with a semiconductor process shrink from 0.6 μ m to 0.5 μ m triple-layer metal technology, the result is a new -3 speed-grade that offers up to a 30% performance improvement over the -5 speed grade (see table).

The XC5200 family delivers 2,000 to 23,000 usable gates and robust features, such as the VersaRing™ I/O interface that maximizes overall utilization and pin assignment flexibility, dedicated JTAG boundary scan logic for increased testability, and

XC5200 Performance Benchmarks

	XC5210-5	XC5210-3	%Improvement
16-Bit Decoder	8 ns	6 ns	25%
24-Bit Accumulator	39 MHz	50 MHz	28%
16-to-1 Multiplexer	13 ns	9 ns	31%
16-Bit Adder	20 ns	15 ns	25%
16-Bit Up/Down Counter	50 MHz	65 MHz	30%

fast carry logic for high speed arithmetic functions. The XC5200 FPGA family continues to lead the industry as the lowest cost/gate programmable logic solution. ♦